

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Shri Vaishnav Institute of Technology and Science Choice Based Credit System (CBCS) in the Light of NEP-2020 Diploma(Electrical Engineering)

(2023-2026)

			TEACHING &EVALUATION SCHEME									
COMPGE			T	HEORY		PRAC'	ΓΙCAL					
COURSE CODE	CATEGORY	COURSE NAME	END SEM University Exam	Two Term Exam	Teachers Assessment*	END SEM University Exam	Teachers Assessment*	L	Т	P	CREDITS	
DTEE601N	DCC	Control System	60	20	20	30	20	3	1	2	5	

Course Objectives:

- 1. To give the concept of control system & its applications in various fields.
- 2. To learn concept of modeling of various physical systems.
- 3. To learn about the performance characteristics and limitations associated with various devices.

Course Outcomes:

After the successful completion of this course students will be able to

- 1. Demonstrate an understanding of the fundamentals of (feedback) control systems.
- 2. Determine and use models of physical systems in forms suitable for use in the analysis and design of control systems.
- 3. Apply root-locus technique to analyze and design control systems.
- 4. Determine the (absolute) stability of a closed-loop control system.

Syllabus

UNIT-I 8 Hrs.

Introduction: Basic concept of open loop and closed loop control system and their comparison- Simple Mathematical model of physical systems-Analogy between different systems-Mechanical and Electrical.

UNIT-II 8 Hrs.

Control System Representation: Transfer function, block diagram, reduction of block diagram, Mason's gain formula, Simple Mathematical problems on block diagram and signal flow graphs.

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Chairperson Faculty of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Controller of Examinations Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

^{*}Teacher Assessment shall be based following components: Quiz/Assignment/ Project/Participation in Class, given that no component shall exceed more than 10 marks.



Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Shri Vaishnav Institute of Technology and Science Choice Based Credit System (CBCS) in the Light of NEP-2020 Diploma(Electrical Engineering)

(2023-2026)

			TEACHING &EVALUATION SCHEME									
COMPGE			T	HEORY		PRAC'	ΓΙCAL					
COURSE CODE	CATEGORY	COURSE NAME	END SEM University Exam	Two Term Exam	Teachers Assessment*	END SEM University Exam	Teachers Assessment*	L	Т	P	CREDITS	
DTEE601N	DCC	Control System	60	20	20	30	20	3	1	2	5	

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P - Practical; C - Credit; *Teacher Assessment shall be based following components: Quiz/Assignment/ Project/Participation in Class, given that no component shall exceed more than 10 marks.

UNIT-III 9 Hrs.

Time Domain Analysis: First and Second order control System (Without mathematical treatment)-Definition of different performance indices as delay time, rise time, peak time, percentage peak overshoot, Settling time, steady state error.-Type-0, Type-1, type-2, system definition-Concept of stability: absolute stability, relative stability-Routh and Hurwitz Criteria for stability.

UNIT-IV 8 Hrs.

Root Locus Techniques: Introduction-Root Locus concept, Construction of Root Loci.

UNIT-V 9 Hrs.

Frequency Domain Analysis: Introduction- Nyquist Stability Criteria and Bode plots of simple control system.

References:

- 1. I.J. Nagrath and M. Gopal, "Control system Engineering", New Age International.
- 2. Control Systems by Ashfaq Hussain, Haroon Ashfaq, Dhanpat Rai& Co.
- **3.** Rudra Pratap, Getting Started with MATLAB, Oxford.
- **4.** Modern Control Systems by Roy Chaudhary. PHI
- **5.** Feedback Control Systems by Dr. S.D. Bhide, R.A. Barapate, S. Satyanarayan, Tech-Max Publication, Pune

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Chairperson Faculty of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Controller of Examinations Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore



Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Shri Vaishnav Institute of Technology and Science Choice Based Credit System (CBCS) in the Light of NEP-2020 Diploma(Electrical Engineering)

(2023-2026)

			TEACHING &EVALUATION SCHEME									
COURSE			Т	HEORY		PRAC'	ΓICAL					
CODE	CATEGORY	COURSE NAME	END SEM University Exam	Two Term Exam	Teachers Assessment*	END SEM University Exam	Teachers Assessment*	L	Т	P	CREDITS	
DTEE601N	DCC	Control System	60	20	20	30	20	3	1	2	5	

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P - Practical; C - Credit; *Teacher Assessment shall be based following components: Quiz/Assignment/ Project/Participation in Class, given that no component shall exceed more than 10 marks.

List of Experiments:

- 1. Determination of transfer function of A-C servomotor
- 2. Determination of transfer function of D-C motor.
- 3. Study of Block diagram reduction Method using MATLAB
- 4. To Plot Root Locus using MATLAB.
- 5. To Plot Nyquist plot using MATLAB
- 6. To Plot Bode plot using MATLAB
- 7. Effect of adding poles on root loci of type-1, type-2 systems through MATLAB.
- 8. Effect of adding zeros on root loci of type-1, type-2 systems through MATLAB.
- 9. Effect of adding poles on bode plots of type-1, type-2 systems through MATLAB.
- 10. Effect of adding zeros on bode plots of type-1, type-2 systems through MATLAB.

Chairperson Board of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Chairperson Faculty of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Controller of Examinations Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore



Diploma EI/EX w.e.f. 2024

		•		TE	ACHIN	G &EVAL	UATIO	N SCH	EME		
			. TI	IEORY		PRACT	ICAL				
COURSE CODE	CATE- GORY	COURSE NAME	END SEM University Exam	Two Term Exam	Teachers Assessment*	END SEM University Exam	Teachers Assessment*	L	Т	P	CREDITS
DTET502	DC	Embedded Systems	60	20	20	30	20	3	0	2	4

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P - Practical; C - Credit.

Course Educational Objectives (CEOs):

- 1. To introduce students to smart devices and how they work in everyday life (like washing machines, mobile phones, and cars).
- 2. To teach programming of small computers (microcontrollers) that control real-world devices.
- 3. To develop skills in connecting sensors, displays, and motors to create intelligent systems.
- 4. To build practical projects that solve real problems using embedded technology.

Course Outcomes (COs):

After completing this course, students will be able to:

- 1. Understand embedded systems, working smart devices around us work and their applications.
- 2. Write simple microcontroller programs to control LEDs, motors, and sensors.
- 3. Interface hardware and connect different components like displays, keyboards, and sensors to microcontrollers.
- 4. Design practical projects like digital clocks, temperature controllers, and security systems.

Syllabus

UNIT I

Introduction to Embedded system. Characteristics: Real-time operation, low power, small size, reliability Types of embedded systems, Applications in daily life, Basic architecture: Processor, Memory, Input/Output, Software. Difference between general-purpose computers and embedded systems

UNIT II 8 Hrs.

Introduction to 8051 Microcontroller: Why 8051 is popular for learning Pin diagram and basic architecture CPU, Memory (RAM, ROM), Timers, Interrupts Memory organization: Program memory vs Data memory Special function registers (SFRs) Input/Output ports: Port 0, 1, 2, 3 functions Reading switches and controlling LEDs Addressing modes: Immediate, Direct, Indirect addressing (with simple examples) Basic instruction set: Data transfer, arithmetic, logical, branch instruction limitations.

Chairperson Board of Studies

Board of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Chairperson

Faculty of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Controller of Examinations

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Donietrar

^{*}Teacher Assessment shall be based on the following components: Quiz/Assignment/ Project/Participation in Class, given that no component shall exceed more than 10 marks.

Diploma EI/EX w.e.f. 2024

		Dipionia Di/Dir wow 2021									
		g		TE	ACHIN	G &EVAL	UATIO	N SCI	HEME		
			T	HEORY		PRACT	ICAL				
COURSE CODE	CATE- GORY	COURSE NAME	END SEM University Exam	Two Term Exam	Teachers Assessment*	END SEM University Exam	Teachers Assessment*	L	Т	P	CREDITS
DTET502	DC	Embedded Systems	60	20	20	30	20	3	0	2	4

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P - Practical; C - Credit.

UNIT III 8 Hrs.

Programming in C: Basic C programs for 8051, Variables, loops, functions for embedded systems, Basic interfacing.

UNIT IV 8 Hrs.

Communication basics: Serial communication (UART), I2C protocol SPI protocol, Analog to Digital Converter, Digital to Analog Converter, Interrupts, Timer and counters.

UNIT V 10 Hrs.

Real-time concepts: What is real-time? Hard vs Soft real-time systems Task scheduling basics Power management: Sleep modes and power saving.

Text Books:

- 1. Mazidi M. A., "The 8051 Microcontroller and Embedded Systems", Pearson Education, 2nd Edition, 2013.
- 2. Rajkamal, "Embedded Systems: Architecture, Programming and Design", TMH, 3rd Edition, 2014.

Reference Books:

- 1. Shibu K. V., "Introduction to Embedded Systems", TMH, 1st Edition, 2009.
- 2. Lyla B. Das, "Embedded Systems: An Integrated Approach", Pearson Education, 1st Edition, 2013.
- 3. Frank Vahid, "Embedded System Design: A Unified Hardware/Software Introduction", Wiley, 2nd Edition, 2002.

List of Experiments:

- 1. Basic LED Control Blinking LED, LED patterns, traffic light simulation.
- 2. Switch Interfacing Reading push buttons, toggle switches.
- 3. 7-Segment Display Single digit display, counter, digital clock.

Chairperson

Board of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Controller of Examinations

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

^{*}Teacher Assessment shall be based on the following components: Quiz/Assignment/ Project/Participation in Class, given that no component shall exceed more than 10 marks.



Diploma EI/EX w.e.f. 2024

			Dipioma Enter	I/EA W.C.1. 2021								
ſ					TE.	ACHIN	G &EVAL	UATION	SCH	EME		
			∞	ŢŢ	IEORY		PRACT	ICAL				
	COURSE CODE	CATE- GORY	COURSE NAME	END SEM University Exam	Two Term Exam	Teachers Assessment*	END SEM University Exam	Teachers Assessment*	L	Т	P	CREDITS
	DTET502	DC	Embedded Systems	60	20	20	30	20	3	0	2	4

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P - Practical; C - Credit. *Teacher Assessment shall be based on the following components: Quiz/Assignment/ Project/Participation in Class, given that no component shall exceed more than 10 marks.

- 4. LCD Interfacing Text display, scrolling messages, menu system.
- 5. Keypad Interfacing 4x4 keypad, password entry system.
- 6. Temperature Monitoring LM35 sensor, digital thermometer with alarm.
- 7. Motor Control DC motor speed control, stepper motor control.
- 8. Serial Communication Data transfer between microcontroller and PC.
- ADC Application Light intensity measurement, voltage monitoring.
- 10. Mini Project Complete system (Security alarm, Automatic water level controller, Digital clock with alarm).

Chairperson

Board of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Chairperson Faculty of Studies Shri Vaishnav Vidyapeeth

Vishwavidyalaya, Indore

Controller of Examinations

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore



Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Shri Vaishnav Institute of Technology and Science Choice Based Credit System (CBCS) in the Light of NEP-2020 **Diploma (Electrical Engineering)** (2021-2024)

		16	TEACHING & EVALUATION SCHEME										
COURSE			Т	HEORY		PRAC	TICAL			Τ			
COURSE CODE	CATEGORY	COURSE NAME	END SEM University Exam	Two Term Exam	Teachers Assessment*	END SEM University Exam	Teachers Assessment*	L T	P	CREDITS			
DTEE502	DCC	Industrial Electronics	60	20	20	30	20	3	0	2	4		

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P - Practical; C - Credit; *Teacher Assessment shall be based following components: Quiz/Assignment/ Project/Participation in Class, given that no component shall exceed more than 10 marks.

Course Objectives:

- 1. Understand the principles, merits and de-merits of Induction/ Di-electric heating
- Draw and design regulated / controlled power supply, SMPS and UPS

Course Outcomes:

After the successful completion of this course students will be able to

- 1. Understand solid state devices as logic switches, power controller switches.
- 2. Understand heating and its properties.
- 3. List general and industrial applications of converters, invertors, choppers, and regulator.
- 4. Select proper device for a given application

Syllabus

UNITI

Inverter Application

6 Hrs.

SMPS Types, Block diagram of SMPS, Various schemes of SMPS, advantages and disadvantages. UPS-Type (ON Line, OFF Line) and its comparison. Battery banks.

UNIT II

Electric Welding

9 Hrs.

Electric welding, resistance and arc welding, control devices and welding equipment. A.C. / D.C. timers using solid state devices, Synchronous and non synchronous timers, Sequence timer, Duty cycle of welding process, Electronic welding controls, SCR as electronic contactor in

UNIT III

High frequency heating

9 Hrs.

Induction Heating: Basic Principle ,Factors Governing the process, Applications, merits &demerits over other systems, Di-electric heating: Basic Principle, Factors governing the process, applications, merits & demerits over other systems.

Chairperson Board of Studies

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Chairperson Faculty of Studies

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Controller of Examinations

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore



Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Shri Vaishnav Institute of Technology and Science Choice Based Credit System (CBCS) in the Light of NEP-2020 Diploma (Electrical Engineering) (2021-2024)

0		J		TEA	CHING	& EVALU	ATION S	CHEM	Œ		
			Т	HEORY		PRAC	TICAL				
COURSE CODE	CATEGORY	COURSE NAME	END SEM University Exam	Two Term Exam	Teachers Assessment*	END SEM University Exam	Teachers Assessment*	L	T	P	CREDITS
DTEE502	DCC	Industrial Electronics	60	20	20	30	20	3	0	2	4

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P - Practical; C - Credit; *Teacher Assessment shall be based following components: Quiz/Assignment/ Project/Participation in Class, given that no component shall exceed more than 10 marks.

UNIT IV

General Applications

9 Hrs.

Static Switches, AC switches, DC Switches, Solid State Relays, DC Solid State Relays, AC Solid State Relays. Static Circuit Breakers, Static AC Circuit Breakers, Static DC Circuit Breakers, Battery Charger, Sawtooth generators, Flasher Circuits

UNIT V

Industrial Applications

9 Hrs.

HVDC Transmission, Types of HVDC link, Bipolar HVDC System, Temperature control, Liquid level controllers, Alarm actuator, High frequency welding, Ultrasonic Applications, Emergency Lighting System.

References:

- 1. Power Electronics by M. H. Rashid PHI Publication-3 rd Edition.
- 2. Industrial Electronics and control by Biswanath Paul, PHI publications2nd Edition.
- 3. Programmable Logic Controllers "Frank D.Petruzela "PHI publications
- 4. Power Electronics by Dr.P. S. Bimbhra, Khanna publishers -2 nd Edition.
- 5. Industrial & Power Electronics By Harish C.Rai, Umesh Publication, 5 th Edition.
- 6. Programmable Logic Controller Pradeep Kumar & Srivashtava- BPB Publications

List of Practical

- 1. Demonstration of SMPS.
- 2. Demonstration of UPS
- 3. Demonstration of High frequency heating
- 4. Demonstration of induction heating.
- 5. Demonstration of Sawtooth generators.
- 6. Study of circuit breaker.

Chairperson Board of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Controller of Examinations
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore



Shri VaishnavVidyapeethVishwavidyalaya, Indore Shri Vaishnav Institute of Technology and Science Choice Based Credit System (CBCS) in the Light of NEP-2020 Diploma(All Branches) w.e.f. 2023

		ix.	- TEACHING &EVALUATION SCHEME								
COURSE CATEG		THEORY			PRACT	TICAL					
COURSE CODE	CATEG ORY	COURSE NAME	END SEM University Exam	Two Term Exam	Teachers Assessment*	END SEM University Exam	Teachers Assessment*	L	Т	P	CREDITS
DTET504		PLC Lab	0	0	0	30	20	0	0	4	2

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P - Practical; C - Credit; *Teacher Assessment shall be based following components: Quiz/Assignment/ Project/Participation in Class, given that no component shall exceed more than 10 marks.

Course Educational Objectives (CEOs):

To understand the concept of:

- 1. Fundamentals of Programmable Logic Controllers and their industrial applications.
- 2. PLC programming using Ladder Logic and basic programming languages.
- 3. Interfacing input/output devices with PLC systems.

Course Outcomes (COs):

After completing the course students will be able to:

- 1. Explain the architecture and working principles of PLC systems.
- 2. Develop ladder logic programs for basic industrial automation applications.
- 3. Interface sensors, actuators, and control devices with PLC.
- 4. Troubleshoot and commission simple PLC-based control systems.

List of Experiments:

- 1. Study of PLC hardware architecture and I/O modules.
- 2. Introduction to PLC programming software and ladder logic basics.
- 3. Programming exercises on basic logic gates (AND, OR, NOT, NAND, NOR) using PLC.
- 4. Programming for motor control applications:
 - Single motor start/stop control
 - Forward-reverse motor control
- 5. Programming for timers and counters:
 - o ON-delay timer application
 - OFF-delay timer application
 - o Up counter and down counter applications
- 6. Design and implementation of sequential control circuits using PLC.
- 7. Traffic light control system simulation using PLC.
- 8. Automatic star-delta starter control using PLC.
- 9. Conveyor belt control system using PLC.
- 10. Temperature monitoring and control using PLC with analog inputs.

Chairperson

Board of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Chairperson

Faculty of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Controller of Examinations

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Registrar

Diploma(All Branches) w.e.f. 2023

-												
		Del	TEACHING &EVALUATION SCHEME									
			Т	HEORY		PRACT	ICAL					
COURSE CODE	CATEG ORY	COURSE NAME	END SEM University Exam	Two Term Exam	Teachers Assessment*	END SEM University Exam	Teachers Assessment*	L	Т	P	CREDITS	
DTET504		PLC Lab	0	0	0	30	20	0	0	4	2	

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P - Practical; C - Credit; *Teacher Assessment shall be based following components: Quiz/Assignment/ Project/Participation in Class, given that no component shall exceed more than 10 marks.

Text Books:

- 1. Frank D. Petruzella, "Programmable Logic Controllers", 6th Edition, McGraw-Hill Education, 2022.
- 2. M. T. White, "PLCs for Beginners: An Introductory Guide to Building Robust PLC Programs with Structured Text", Packt Publishing, 2024.

Reference Books:

- 1. Gary A. Dunning, "Introduction to Programmable Logic Controllers", 3rd Edition, Delmar Cengage Learning, 2006.
- 2. Ashraf Said AlMadhoun, "PLC SCADA for Beginners: Understanding and Implementing Industrial Automation Systems", Springer, 2023.
- 3. Himanshu Kumar, "Advanced Industrial Automation: PLC Programming in Simplest Way with 110 Solved Examples", Notion Press, 2020.
- 4. R. G. Jamkar, "Industrial Automation Using PLC SCADA & DCS", Dreamtech Press, 2019.

Chairperson
Board of Studies

Board of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Chairperson

Faculty of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Controller of Examinations

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Registrar