

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Shri Vaishnav Institute of Technology and Science, Indore

Time Table (ATKT)

VI Semester End Semester Examination December 2025 - January 2026


Time: 01:00 PM to 04:00 PM (Afternoon Shift)

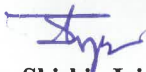
Date/Day	B.Tech (Electronics & Communication Engineering)	B.Tech (Electronics & Communication - IOT)	B.Tech (Electrical Engineering)	B.Tech (Electrical & Electronics)	B.Tech (Mechanical Engineering)	B.Tech (Automobile Engineering)
05/01/2026		BTECIOT602			BTME602A	BTAU602C
Monday		DATA ANALYTICS FOR IOT			ADVANCED PRODUCTION TECHNOLOGY	MATERIAL HANDLING AND STORAGE SYSTEMS
07/01/2026	GE-VI*	GE-VI*	GE-VI*	GE-VI*	GE-VI*	GE-VI*
Wednesday	GENERIC ELECTIVE-VI	GENERIC ELECTIVE-VI	GENERIC ELECTIVE-VI	GENERIC ELECTIVE-VI	GENERIC ELECTIVE-VI	GENERIC ELECTIVE-VI
09/01/2026		BTEC714			BTME605N	BTAU602
Friday		EMBEDDED SYSTEM FOR ROBOTICS			DESIGN OF MACHINE COMPONENTS AND TOOLS	CHASSIS AND BODY ENGINEERING
12/01/2026	BTEC603	BTEC603	BTEE603	BTEC603	BTME601A	BTME601A
Monday	DISCRETE TIME SIGNAL PROCESSING	DISCRETE TIME SIGNAL PROCESSING	UTILIZATION OF ELECTRICAL ENERGY	DISCRETE TIME SIGNAL PROCESSING	OPERATION RESEARCH AND SUPPLY CHAIN	OPERATION RESEARCH AND SUPPLY CHAIN
14/01/2026		BTECIOT601			BTME604	BTAU605
Wednesday		APPLICATION DEVELOPMENT FOR IOT			HEAT AND MASS TRANSFER	AUTOMOTIVE COMPONENT DESIGN
16/01/2026					BTME603	
Friday					MECHANICAL VIBRATION	

*Students are advised to check their internal marks before appearing in the end sem examination

The syllabus for ATKT students shall be as per the revised (updated) syllabus.

Students are advised to visit www.svvv.edu.in (University Website) for all informations related Theory exam Time Table (Regular / ATKT)


Dr. Nilesh Patidar
Assistant COE


Dr. Shishir Jain
Controller of Examinations