



Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Shri Vaishnav Institute of Technology & Science, Indore

Choice Based Credit System (CBCS) Scheme

B. Tech. (Electronics and Instrumentation)

w.e.f July 2017

IV - SEMESTER

S. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/week			Examination Scheme					Total Marks	CREDITS
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Assessment*	End Sem University Exam	Teachers Assessment*		
1	BTMA301	Applied Mathematics-3		3	1	0	60	20	20	0	0	100	4
2	BTEI401	Microprocessor and Micontroller		3	1	2	60	20	20	30	20	150	5
3	BTEI402	Sensor and Transducer		3	1	2	60	20	20	30	20	150	5
4	BTEC401	Linear integrated circuit		3	1	2	60	20	20	30	20	150	5
5	BTEC408	Signal and System		3	1	2	60	20	20	30	20	150	5
6	BTEI403	PCB Designing Lab-II		0	0	2	0	0	0	30	20	50	1
7	BTPD102	Personality development-2		0	0	2	0	0	0	0	50	50	1
TOTAL				15	5	12	300	100	100	150	150	800	26

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Chairperson
Board of Studies

Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore

Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore