

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Shri Vaishnav Institute of Technology & Science, Indore

Choice Based Credit System (CBCS) Scheme

Diploma in Electronics and Instrumentation Engineering

w.e.f.July2017

I - SEMESTER **Teaching Scheme/week Examination Scheme** Theory Practical Course Total Subject Code S. No. Name of Subject CREDITS End Sem Two **End Sem** P L Т Code Teachers Teachers Marks University Term University Assessment* Assessment* Exam Exam Exam Applied Mathematics-1 1 DTMA101 3 0 0 60 20 20 0 0 100 3 2 DTPH101 **Applied** Physics 3 0 2 60 20 20 30 20 150 4 3 **DTME101** Engineering Drawing 3 0 4 60 20 20 30 . 20 150 5 4 DTME102 Basic Mechanical Engineering 3 0 2 60 20 20 30 20 150 4 5 DTHU101 Communication Skill 0 0 4 0 0 0 30 2 20 50 Fundamental of Instruments and 6 **DTEI102** 2 0 0 60 20 20 0 0 100 2 Control 7 **DTME103** Workshop Practices 0 0 4 0 0 0 30 20 2 50 TOTAL 14 0 16 300 100 100 150 100 750 22

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Chairperson Board of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya Indore

Shri Vaishnav Vidy peeth Vishwavidyalaya, Indore

Joint Registrar Shri Vaishnav Vidyapeeth Vishwayidyalaya, Indore