



Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Shri Vaishnav Institute of Technology & Science, Indore

Choice Based Credit System (CBCS) Scheme

Diploma in Electronics and Instrumentation Engineering

w.e.f. July 2017

III - SEMESTER

S. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/week			Examination Scheme					Total Marks	CREDITS
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Assessment*	End Sem University Exam	Teachers Assessment*		
1	ML301	Environmental and Energy studies		4	0	0	60	20	20	0	0	100	4
2	DTEI301	Measurement & Instrumentation		2	1	2	60	20	20	30	20	150	4
3	DTEI302	Digital Electronics		3	1	2	60	20	20	30	20	150	5
4	DTEE101	Basic Electrical Engineering		2	1	2	60	20	20	30	20	150	4
5	DTCS201	Computer Application-II		0	0	4	0	0	0	30	20	50	2
6	DTEI303	Seminar		0	0	2	0	0	0	30	20	50	1
7	DTEI304	Electronics Workshop-1		0	0	4	0	0	0	60	40	100	2
TOTAL				11	3	16	240	80	80	210	140	750	22

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.



Chairperson
Board of Studies

Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore



Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore