



Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Shri Vaishnav Institute of Technology & Science, Indore

Choice Based Credit System (CBCS) Scheme

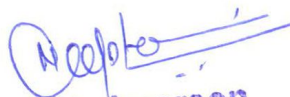
Diploma in Electronics and Instrumentation Engineering

w.e.f. July 2017

IV- SEMESTER

S. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/week			Examination Scheme					Total Marks	CREDITS
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Assessment*	End Sem University Exam	Teachers Assessment*		
1	DTMA301	Applied Mathematics-3		3	1	0	60	20	20	0	0	100	4
2	DTEI401	Instrumentation & Process Control		2	1	2	60	20	20	30	20	150	4
3	DTEI402	Microprocessors 8085 & Interfacing		2	1	2	60	20	20	30	20	150	4
4	DTEI403	Electronic Devices and Circuits		2	1	2	60	20	20	30	20	150	4
5	DTEI404	Electro Magnetic Theory		3	1	0	60	20	20	0	0	100	4
6	DTEI405	PLC Lab		0	0	4	0	0	0	60	40	100	2
TOTAL				12	5	10	300	100	100	150	100	750	22

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.


Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore


Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore