



# Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

## Shri Vaishnav Institute of Technology & Science, Indore


### Choice Based Credit System (CBCS) Scheme

### Diploma in Electronics and Instrumentation Engineering

#### V - SEMESTER

S. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/week			Examination Scheme					Total Marks	CREDITS
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Assessment*	End Sem University Exam	Teachers Assessment*		
1	DTEI501	Data Communication & Networks		2	1	2	60	20	20	30	20	150	4
2	DTEI502	Control System Engineering		2	1	2	60	20	20	30	20	150	4
3	DTEI503	Microprocessor and Microcontroller		2	1	2	60	20	20	30	20	150	4
4	DTEI504	Analytical Instrumentation		2	1	2	60	20	20	30	20	150	4
5	DTEI505	Generation , Transmission and Distribution of Power		3	1	0	60	20	20	0	0	100	4
6	DTEI506	MATLAB & LABVIEW		0	0	2	0	0	0	30	20	50	1
7	DTEI507	Minor Project		0	0	2	0	0	0	30	20	50	1
TOTAL				11	5	12	300	100	100	180	120	800	22

\*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.



Chairperson  
Board of Studies

Shri Vaishnav Vidyapeeth Vishwavidyalaya  
Indore



Joint Registrar  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore