



Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Shri Vaishnav Institute of Technology & Science, Indore

Choice Based Credit System (CBCS) Scheme

Diploma in Electronics and Instrumentation Engineering

VI - SEMESTER

S. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/week			Examination Scheme					Total Marks	CREDITS
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Assessment*	End Sem University Exam	Teachers Assessment*		
1	DTEI601	VLSI & VHDL		2	1	2	60	20	20	30	20	150	4
2	DTEI602	Industrial Management & Enterprenuership		2	1	0	60	20	20	0	0	100	3
3	DTEI603	Bio-Medical Instrumentation		2	1	2	60	20	20	30	20	150	4
4	DTEI604	Transducer & Signal Conditioning		2	1	2	60	20	20	30	20	150	4
5	DTEI611/612/613	Elective		3	1	2	60	20	20	30	20	150	5
6	DTEI605	Major Project		0	0	4	0	0	0	60	40	100	2
TOTAL				11	5	12	300	100	100	180	120	800	22

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Elective	1. Fiber Optics(611)		3	1	2	60	20	20	30	20	150	5
	2. Digital Image Processing(612)		3	1	2	60	20	20	30	20	150	5
	3. Linear integrated circuit(613)		3	1	2	60	20	20	30	20	150	5



Chairperson
Board of Studies

Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore



Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore