

Shri Vaishnav Vidyapeeth Vishwavidyalaya

Master of Technology (Power Electronics) Choice Based Credit System (CBCS)

SEMESTER - II(2018-19)

	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK				EXAMINATION SCHEME				S	
S. No.			L		ГР	CREDITS	THEORY			PRACTICAL		MARI
				T			End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	TOTAL MARKS
1	MTPE201	Power Quality	3	1	2	5	60	20	20	30	20	150
2	MTPE202	Analysis of Powe Electronics CktsII	3	1	2	5	60	20	20	30	20	150
3	MTPE203	Advanced Micro Processor and Micro Controller	3	1	0	4	60	20	20	0	0	100
4		Elective-I	3	1	0	4	60	20	20	0	0	100
5		Elective-II	3	1	0	4	60	20	20	0	0	100
6	MTPE204	Power Electronics Lab-II	0	0	4	2	0	0	0	0	50	50
7	MTPE205	Comprehensive VIVA	0	0	8	4	0	0	0	200	0	200
8	MTPE206	Seminar/GD	0	0	4	2	0	0	0	0	50	50
			15	5	20	30	300	100	100	260	140	900

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P - Practical;

^{*}Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

		Elective-I
1	MTPE214	Advanced Electric Drives
2	MTPE224	Switched mode Power Converters
3	MTPE234	Neural Networks and Fuzzy

		Elective-II
1	MTPS226	Smart Grid Technologies and Application
2	MTPS227	Energy management in Electrical System
3	MTPS215	Power System Stability and Control

Chairperson **Board of Studies** Shri Vaishnav Vidyapeeth Vishwavidyalaya Indore

Shri Valshnav Vidyapeeth Vishwavidyalaya