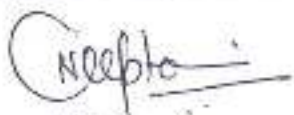


Shri Vaishnav Vidyapeeth Vishwavidyalaya													
Choice Based Credit System (CBCS)													
Master of Technology (Power Electronics)(Session 2018-19)													
IV-SEMESTER													
Sn	Subject Code	Subject Name	Course Code	Teaching Scheme/week			Examination Scheme					Total Marks	Total Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Assessment*	End Sem University Exam	Teachers Assessment*		
1	MTPS401	Dissertation Phase-II		0	0	40	0	0	0	300	200	500	20
Total				0	0	40	0	0	0	300	200	500	20


 Chairperson
 Board of Studies
 Shri Vaishnav Vidyapeeth Vishwavidyalaya
 Indore


 Registrar
 Shri Vaishnav Vidyapeeth Vishwavidyalaya
 INDORE (M.P.)