

Shri Vaishnav Vidyapeeth Vishwavidyalaya Shri Vaishnav Institute of Technology and Science Choice Based Credit System (CBCS) Scheme in the light of NEP-2020 B.Tech. in Electronics and Communication (Internet of Things)

		SEM	-			21-20	25)			an a		
S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK				EXAMINATION SCHEME				-	ks
						CREDITS	THEORY		PRACTICAL		IAR	
			L	Т	Р		End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assess- ment* (20%)	End Sem University Exam (60%)	Teachers Assess- ment* (40%)	TOTAL MARKS
1	BTEI401	Microprocessor and Microcontroller	3	1	2	5	60	20	20	30	20	150
2	BTECIOT501	Communication Systems	3	1	2	5	60	20	20	30	20	150
3	BTECIOT603	IOT Architecture and Protocols	3	0	0	3	60	20 .	20	0	0	100
4	-	Elective I	3	1	2	5	60	20	20	30	20	150
5	-	MOOCs II (Generic)	-	-	-	2	ч <u>-</u>	-	-	-	50	50
6	BTME510	Design Thinking and Innovation	2	0	0	2	60	20	20	0	0	100
7	BTECIOT503	Minor Project	0	0	4	2	0	0	0	30	20	50
8	BTEC507	Programming in Python	0	0	2	1	0	0	0	30	20	
TOTAL Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P -			14 Practic	3	12	25	300	100	100	150	150	800
Teac	ner Assessment sl	nall be based on following components: Quiz/Assig pulsorily opt MOOCS course of two credit from S	nment/	Project	/Patici _l	ation i	n Class, given	that no co	mponent sh	all exceed mo	ore than 10 n	narks.

S No Course Code Elective I BTCS405 1 Data Base Management System 2 BTEC515 Data Communication and Computer Networks BTEI611 3 Data Acquisition System Ditt Neebto Chairperson Chairperson Controller of Kaminations **Board of Studies** Faculty of Studies Shri Vaishnav Vidyapeeth Shri Vaishnav Vidyapeeth Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Vishwavidyalaya, Indore Vishwavidyalaya, Indore

Joint Registrar Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

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Vice Chancellor Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore