



Shri Vaishnav Vidyapeeth Vishwavidyalaya
Shri Vaishnav Institute of Technology and Science
Choice Based Credit System (CBCS) Scheme in the light of NEP-2020
B.Tech. in Electronics and Communication

SEMESTER-II (2023-2027)

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assess-ment* (20%)	End Sem University Exam (60%)	Teachers Assess-ment* (40%)	
1	BTME105	Fundamentals of Mechanical Engineering and Applied Mechanics	3	0	2	4	60	20	20	30	20	150
2	BTPH101	Applied Physics	3	1	2	5	60	20	20	30	20	150
3	BTMA201N	Mathematics II	3	1	0	4	60	20	20	0	0	100
4	BTEC104	Digital Logic and Circuit Design	3	1	2	5	60	20	20	30	20	150
5	BTEC103	Electronics Workshop	0	0	2	1	0	0	0	30	20	50
6	BTME103	Workshop Practices	0	0	2	1	0	0	0	30	20	50
TOTAL			12	3	10	20	240	80	80	150	100	650

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical

***Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.**

Chairperson

Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Chairperson

Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Controller of Examinations

Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Joint Registrar

Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Vice Chancellor

Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore