	WINNAULU Manual Control of the Angle of the	Shri Vaishna Shri Vaishnav I Choice Based Credit Syste B.Tech. in E	nstitut em (C] lectro	e of BCS) nics a	Fechn Sche Ind C	ology me in ommu	and Scient the light inication	ence t of N	EP-202()		
		SEW		R-V Eachi <u>EME/V</u>	NG		Í	EXAMI	NATION S	CHEME		S
S. No.	COURSE CODE	COURSE NAME	L	Т	Р	CREDITS	End Sem Unversity Exam	HEORY Two Term Exam	Y Teachers Assess- menț*	PRACT End Sem Unversity Exam		
1	BTEI401	Microprocessor and Microcontroller	3	1	2	5	<u>(60%)</u> 60	(20%) 20	(20%) 20	(60%)	(40%)	
2	BTEC502	Cellular and Mobile Communication	3	0	0	3	60	20	20	30	20	150
3	BTEC504	CMOS VLSI Design	3	1	· 2	5	60	20	20	0	0	100
4	-	Elective I	3	1	2	5	60	20	20	30	20	150
5	-	MOOCs II	-	-	-	2				30	20	150
6	BTME510	Design Thinking and Innovation	2	0	0	2	- 60	- 20	-	-	50	50
7		Minor Project	0	0	4	2			20	0	0	100
8	BTEC507	Programming in Python				2	0	0	0	30	20	50
		ΤΟΤΑΙ	0	0	2	1	0	0	0	30	20	50
Legend	s: L - Lecture	vity · D	Dugatian				100	100	150	150	800	
Teach	er Assessment	; T - Tutorial/Teacher Guided Student Activ shall be based on following components: Qu npulsorily opt MOOCS course of two credit	iz/Accian	mont/D	nain at/D	25 articipa	300 tion in Class					

S. No.	Course Code	Elective I
1	BTEC525	FPGA Based System Design
2	BTEI611	Data Acquisition Systems
3		Data Structures and Algorithm

Neepto Chairperson Board of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

NIODIO Chairperson

Faculty of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Controller of Examinations

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Joint Registrar Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

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Vice Chancellor Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore