



Shri Vaishnav Vidyapeeth Vishwavidyalaya
Shri Vaishnav Institute of Technology and Science
Choice Based Credit System (CBCS) Scheme in the light of NEP-2020
B.Tech. in Electrical and Electronics Engineering

SEMESTER-IV (2023-2027)

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	BTEI401	Microprocessor and Microcontroller	3	1	2	5	60	20	20	30	20	150
2	BTEE401	Electrical Machines I	3	0	2	4	60	20	20	30	20	150
3	BTEE403	Power System I	3	0	2	4	60	20	20	30	20	150
4	BTEE404	Electromagnetic Field Theory	3	1	0	4	60	20	20	0	0	100
5	ML307	Environmental Management and Sustainability	4	0	0	4	60	20	20	0	0	100
6		Moocs	-	-	-	2	-	-	-	-	50	50
7	BTEC405	Programming with Arduino	0	0	4	2	0	0	0	30	20	50
TOTAL			16	2	10	25	300	100	100	120	130	750

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

* Students have to compulsorily opt MOOCS course of two credit from Swayam Portal

Chairperson

Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Chairperson

Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Controller of Examinations

Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Registrar

Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Vice Chancellor

Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore