		Shri Vaishnav Shri Vaishnav Ins Choice Based Credit Systen	titute 1 (CB	of To CS) S	echno Schen	ology 1e in	and Scie the light	nce	CP-2020		τ.	
		B.Tech. in										
	COURSE CODE	COURSE NAME	STER-VI (2021- TEACHING SCHEME/WEEK			2025	EXAMINATION SCHEME					s
S.						CREDITS	THEORY			PRACTICAL		MARKS
5. No.			L	Т	Р		End Sem Unversity Exam (60%)	Two Term Exam (20%)	Teachers Assess- ment* (20%)	End Sem Unversity Exam (60%)	Teacher s Assess- ment* (40%)	T
1	BTRA601	Advanced Control Systems	3	1	0	4	60	20	20	0	0	100
2	BTECIOT701	Real Time Operating Systems	3	0	2	4	60	20	20	30	20	150
3	BTRA602	Computer Aided Design and Manufacturing	3	1	2	5	60	20	20	30	20	150
4	-	Elective II	3	1	2	5	60	20	20	30	20	150
5	-	Generic Elective III	3	0	0	3	60	20	20	0	0	100
6	-	MOOCs III	-	-	-	2	-	-	-	-	50	
7	BTECIOT604	Embedded Systems with Raspberry pi and Arduino	0	0	2	1	0	0	0	60	40	50. 100
		TOTAL	15	3	8	24	300	100	100	150	150	800
* Teac 10 ma	her Assessment sł rks.	F - Tutorial/Teacher Guided Student Activity ; nall be based on following components: Quiz/A pulsorily opt MOOCS course of two credit from	ssignme	nt/Proje								

 S. No. Course Code
 Elective II

 1
 BTEC515
 Data Communication and Computer Networks

 2
 BTRA613
 Neural Networks and Fuzzy Logic

 3
 BTEC624
 Advanced Microcontrollers

20510

Chairperson Board of Studies Shri Vaishnav Vidyapeeth Vishwavidyataya, Indore

. . Chairperson Faculty of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

P.)

Controller of Examinations Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

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Joint Registrar Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

9.1.2024

Vice Chancellor Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore