



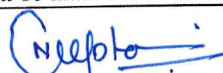
**Shri Vaishnav Vidyapeeth Vishwavidyalaya**  
**Shri Vaishnav Institute of Technology and Science**  
**Choice Based Credit System (CBCS) Scheme in the light of NEP-2020**  
**Diploma in Electrical Engineering**

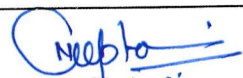
**SEMESTER- IV (2023-2026)**

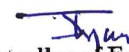
SEMESTER- IV (2023-2024)												
S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assess-ment* (20%)	End Sem University Exam (60%)	Teachers Assess-ment* (40%)	
1	DTEE401N	Power Electronics	3	1	2	5	60	20	20	30	20	150
2	DTEE302	DC Machines	3	0	2	4	60	20	20	30	20	100
3	DTEE305	Generation Transmission and Distribution	3	0	0	3	60	20	20	0	0	100
4	DTEE405N	Fundamentals of Microprocessor	3	0	2	4	60	20	20	30	20	150
5	DTET405	Arduino Lab	0	0	4	2	0	0	0	30	20	50
6	-	Generic Elective II	3	0	0	3	60	20	20	0	0	100
7	-	MOOCs (Generic)	-	-	-	1	0	0	0	0	0	100
TOTAL			15	1	10	22	300	100	100	120	80	750

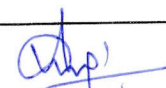
Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

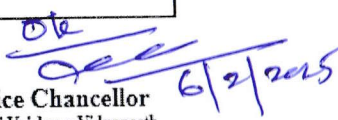
\*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

  
Chairperson  
Board of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

  
Chairperson  
Faculty of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

  
Controller of Examinations  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

  
Registrar  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

  
Vice Chancellor  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore  
6/2/2025