




Shri Vaishnav Vidyapeeth Vishwavidyalaya
Shri Vaishnav Institute of Technology and Science
Choice Based Credit System (CBCS) Scheme in light of NEP-2020
Diploma in Electronics


SEMESTER- IV (2023-2026)

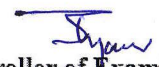
S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assess-ment* (20%)	End Sem University Exam (60%)	Teachers Assess-ment* (40%)	
1	DTET401	Introduction to communication System	3	0	2	4	60	20	20	30	20	150
2	DTMT303	Analog Electronics	3	0	2	4	60	20	20	30	20	150
3	DTEE405N	Fundamentals of Microprocessor	3	0	2	4	60	20	20	30	20	150
4	DTET402	Data Communication and Networks	3	1	0	4	60	20	20	0	0	100
5	DTET405	Arduino Lab	0	0	4	2	0	0	0	30	20	50
6	-	Generic Elective II	3	0	0	3	60	20	20	0	0	100
7	-	Moocs	-	-	-	1	0	0	0	0	0	100
TOTAL			15	1	10	22	300	100	100	120	80	800


Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

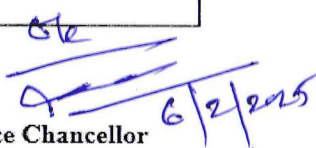
*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.


Chairperson
 Board of Studies
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya, Indore


Chairperson
 Faculty of Studies
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya, Indore


Controller of Examinations
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya, Indore


Registrar
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya, Indore


Vice Chancellor
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya, Indore