



Shri Vaishnav Vidyapeeth Vishwavidyalaya
Shri Vaishnav Institute of Technology and Science
Choice Based Credit System (CBCS) Scheme in the light of NEP-2020
M.Tech. (VLSI Design)

SEMESTER-IV (2025-2027)

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assess-ment* (20%)	End Sem University Exam (60%)	Teachers Assess-ment* (40%)	
1	MTVD401	Dissertation Phase II/ Industrial Project	0	0	32	16	0	0	0	300	200	500
TOTAL			0	0	32	16	0	0	0	300	200	500

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical

* Either Student has to undergo Industrial Project or Dissertation Phase II.

**Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Chyau
20/7/2026

Naeplo

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Naeplo

Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Shyam
Controller of Examinations
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Naeplo
Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Shyam
Vice Chancellor
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore