



**Shri Vaishnav Vidyapeeth Vishwavidyalaya**  
**Shri Vaishnav Institute of Technology and Science**  
**Choice Based Credit System (CBCS) Scheme in the light of NEP-2020**  
**M.Tech. in Embedded System**

**SEMESTER-I (2021-2023)**

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assess-ment* (20%)	End Sem University Exam (60%)	Teachers Assess-ment* (40%)	
1	MTMAEC101	Advanced Mathematics	3	1	0	4	60	20	20	0	0	100
2	MTES124	Embedded System Designing using Raspberry pi	3	0	2	4	60	20	20	30	20	150
3	MTES102	Microcontrollers and Interfacing	2	0	2	3	60	20	20	30	20	150
4	MBAI301C	Advanced Human Values and Professional Ethics	4	0	0	4	60	20	20	0	0	100
5	-	Elective I	3	0	0	3	60	20	20	0	0	100
6	-	Generic Elective	3	0	0	3	60	20	20	0	0	100
TOTAL			18	1	4	21	360	120	120	60	40	700

**Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical**

**\*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.**

Elective I		
S. No.	Course Code	Course Name
1	MTES115	Cryptography and Network Security
2	MTES114	Data Acquisition Systems
3	MTES101	Digital VLSI Design

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Board of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

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**Controller of Examination**  
Shri Vaishnav Vidyapeeth  
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**Joint Registrar**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

**Vice Chancellor**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore