



**Shri Vaishnav Vidyapeeth Vishwavidyalaya**  
**Shri Vaishnav Institute of Technology and Science**  
**Choice Based Credit System (CBCS) Scheme in the light of NEP-2020**  
**B.Tech. in Electronics and Communication (Internet of Things)**

**SEMESTER-II (2021-2025)**

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	BTEC402	Digital Electronics	2	1	2	4	60	20	20	30	20	150
2	BTME105	Fundamentals of Mechanical Engineering and Applied Mechanics	3	0	2	4	60	20	20	30	20	150
3	BTPH101	Applied Physics	3	1	2	5	60	20	20	30	20	150
4	BTMA201	Applied Mathematics II	3	1	0	4	60	20	20	0	0	100
5	ML307	Environmental Management and Sustainability	4	0	0	4	60	20	20	0	0	100
6	BTME103	Workshop Practices	0	0	2	1	0	0	0	30	20	50
7	BTEC103	Electronics Workshop	0	0	2	1	0	0	0	30	20	50
<b>TOTAL</b>			<b>15</b>	<b>3</b>	<b>10</b>	<b>23</b>	<b>300</b>	<b>100</b>	<b>100</b>	<b>150</b>	<b>100</b>	<b>750</b>

**Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical**

**\*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.**

**Chairperson**  
Board of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

**Chairperson**  
Faculty of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

**Controller of Examination**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

**Joint Registrar**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

**Vice Chancellor**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore