## Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)

Choice Based Credit System (CBCS) Scheme

B.Tech in Electronics and Communication with Specialization in IOT VI - SEMESTER(w.e.f. Batch 2018-19)

| Sr.<br>No. | Subject<br>Code | Name of Subject   | Course<br>Code | Teaching Scheme/<br>Week |   |    | Examination Scheme           |                     |                       |                              |     |       |         |
|------------|-----------------|---|----------------|--------------------------|---|----|------------------------------|---------------------|-----------------------|------------------------------|-----|-------|---------|
|            |                 |   |                |                          |   | Р  | Theory                       |                     |                       | Pract                        |     | Total |         |
|            |                 |   |                | L                        | Т |    | End Sem<br>Unversity<br>Exam | Two<br>Term<br>Exam | Teachers Asses- ment* | End Sem<br>Unversity<br>Exam |     | 1 1   | Credits |
| 1          | BTECIOT601      | Application Development for IOT   |                | 3                        | 0 | 2  | 60                           | 20                  | 20                    | 30                           | 20  | 150   | 4       |
| 2          | BTEC603         | Discrete Time Signal Processing   |                | 3                        | 1 | 2  | 60                           | 20                  | 20                    | 30                           | 20  | 150   | 5       |
| 3          | BTECIOT602      | Data Analytics for IOT  |                | 3                        | 0 | 2  | 60                           | 20                  | 20                    | 30                           | 20  | 150   | 4       |
| 4          | BTECIOT603      | IOT Architecture and Protocols  |                | 3                        | 1 | 0  | 60                           | 20                  | 20                    | 0                            | 0   | 100   | 4       |
|            |                 | Elective: 1 FPGA Based System Design 2 Digital Image Processing 3 Embedded Systems for Robotics |                | 3                        | 1 | 2  | 60                           | 20                  | 20                    | 30                           | 20  | 150   | 5       |
| 6          | BTECIOT604      | Embedded System with Raspberry pi and Arduino   |                | 0 (                      | 0 | 2  | 0                            | 0                   | 0                     | 30                           | 20  | 50    | 1       |
| 7          | BTEC507         | Programming in Python   |                | 0                        | 0 | 2  | 0                            | 0                   | 0                     | 30                           | 20  | 50    | 1       |
| TOTAL      |                 |   |                |                          | 3 | 12 | 300                          | 100                 | 100                   | 180                          | 120 | 800   | 24      |

Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Paticipation in Class, given that no component shall exceed more than 10 marks.

Students are advised to take online MOOCS course of minimum two credits

Board of Studies

Shri Vaishnav Vidyapeeth Vishwavidyalaya indore

Chairperson Faculty of Studies Shri Vaishnav Vidyapeeth

Vishwavidyalaya, INDORE

Controller of Examinations Shri Vaishnav Vidyapeeth Vishwavidyalay

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Vice Chancellor Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore