

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)

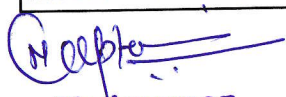
Choice Based Credit System (CBCS) Scheme

B.Tech in Electronics and Communication with Specialization in IOT

VII - SEMESTER

VII - SEMESTER													
Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Asses- ment*	End Sem University Exam	Teachers Asses- ment*		
1	BTECIOT701	Real Time Operating System		3	1	2	60	20	20	30	20	150	5
2	BTECIOT702	Security in IOT		3	1	2	60	20	20	30	20	150	5
3	BTECIOT703	Wireless Sensor Network		3	1	2	60	20	20	30	20	150	5
4	BTEC723/ BTECIOT714/ BTEC701	Elective: 1 Advanced Microcontroller and Embedded Systems 2 Data Analytics 3 Optical and Satellite Communication		3	1	2	60	20	20	30	20	150	5
5	BBAI501	Human Values and Professional Ethics		4	0	0	60	20	20	0	0	100	4
6	BTECIOT705	Major Project-I		0	0	4	0	0	0	50	50	100	2
TOTAL				15	5	12	300	100	100	170	130	800	26

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.



Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore



Registrar
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore