



Shri Vaishnav Vidyapeeth Vishwavidyalaya
Shri Vaishnav Institute of Technology and Science
Choice Based Credit System (CBCS) Scheme in the light of NEP-2020
B.Tech. in Electronics and Communication (AI-IOT)

SEMESTER-III (2025-2029)

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assess-ment* (20%)	End Sem University Exam (60%)	Teachers Assess-ment* (40%)	
1	BTEC301	Advanced Programming Concepts	3	0	2	4	60	20	20	30	20	150
2	BTECIOT301	Introduction to IOT and Embedded Systems	3	0	2	4	60	20	20	30	20	150
3	BTECIOT401	Sensors and Signal Conditioning	3	0	2	4	60	20	20	30	20	150
4	BTEC304	Electronic Devices and Circuits	3	0	2	4	60	20	20	30	20	150
5	-	Generic Elective I	4	0	0	4	60	20	20	0	0	100
6	BTEC308	Design Lab	0	0	2	1	0	0	0	30	20	50
TOTAL			16	0	10	21	300	100	100	150	100	750
Legends: L - Lecture ; T - Tutorial/Teacher - Guided Student Learning ; P - Project/Practical												

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Signature
16/7/2026

Signature
Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Signature
Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Signature
Controller of Examinations
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Signature
Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Signature
Vice Chancellor
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore