



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Bachelor of Technology (Electrical Engineering)

Choice Based Credit System (CBCS)

SEMESTER - VIII (2018 - 2022) TEACHING SCHEMENDER

S. No.	COURSE CODE	COURSE NAME	SCHEME/WEEK				EXAMINATION SCHEME					KS
				L T	Р	CREDITS	THEORY			PRACTICAL		MAR
			L				End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	TOTAL
1	BTEE801	Project / Industrial Internship	0	0	24	12	0	0	0	300	200	500
-	48.8	1.12 1.1 1.1	0	0	24	12	0	0	0	300	200	500

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

ACL

Chairperson Faculty of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, INDORE

Controller of Examinations Shri Vaishnav Vidyapeeth Vishwavidyalay

Regist Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Vice Chancellor Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore



Chairperson Board of Studies In Valshnav Vidyapeeth Vishwavidyalaya Indore