Shri Vaishnav Vidyapeeth Vishwavidyalaya
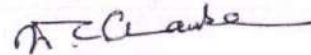
Bachelor of Technology (Electrical & Electronics Engineering)
Choice Based Credit System (CBCS)

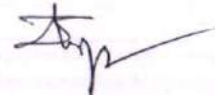## SEMESTER - VIII (w.e.f. Batch 2018-19) (2018 - 2022)

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
							THEORY			PRACTICAL		
			L	T	P		End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	BTEE801	Project / Industrial Internship	0	0	24	12	0	0	0	300	200	500
			0	0	24	12	0	0	0	300	200	500

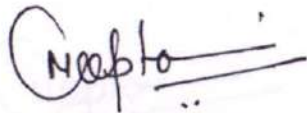Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

A.C.Clarke

Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, INDORE

Controller of Examinations
Shri Vaishnav Vidyapeeth Vishwavidyalay

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore

Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Vice Chancellor
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore