

## Shri Vaishnav Vidyapeeth Vishwavidyalaya Shri Vaishnav Institute of Technology and Science Choice Based Credit System (CBCS) Scheme in the light of NEP-2020 B.Tech. in Electrical Engineering(Tata Power)

## SEMESTER-I (2023-2027)

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK				EXAMINATION SCHEME					uKS
			L	Т	P	CREDITS	THEORY End Sem Two		Teachers	PRAC' End Sem	TICAL Teachers	L MARKS
							Unversity Exam (60%)	Term Exam (20%)	Assess- ment* (20%)	Unversity Exam (60%)	Assess- ment* (40%)	TOTAL
1	BTEE106	Fundamentals of Electrical and Electronics Engineering	3	0	2	4	60	20	20	30	20	150
2	BTME101	Engineering Drawing	1	0	4	3	60	20	20	30	20	150
3	BTCH101	Applied Chemistry	3	1	2	5	60	20	20	30	20	150
4	BTMA101	Applied Mathematics I	3	1	0	4	60	20	20	0	0	100
5	HUCS101	Communication Skills	1	0	2	2	60	20	20	0	20	120
6	BTEE107	Programming for Problem Solving	0	0	4	2	0	0	0	30	20	50
TOTAL			11	2	14	20	300	100	100	120	100	720

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical

Chairperson

Board of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Chairperson

Faculty of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Controller of Examinations

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Joint Registrar

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Vice Chancellor

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

<sup>\*</sup>Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.