



Shri Vaishnav Vidyapeeth Vishwavidyalaya
Shri Vaishnav Institute of Technology and Science
Choice Based Credit System (CBCS) Scheme in the light of NEP-2020
B.Tech. in Electrical and Electronics Engineering

SEMESTER-II (2023-2027)

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	BTME105	Fundamentals of Mechanical Engineering and Applied Mechanics	3	0	2	4	60	20	20	30	20	150
2	BTPH101	Applied Physics	3	1	2	5	60	20	20	30	20	150
3	BTMA201N	Applied Mathematics II	3	1	0	4	60	20	20	0	0	100
4	BTEC104	Digital Logic and Circuit Design	3	1	2	5	60	20	20	30	20	150
5	BTEE103	Electrical Workshop	0	0	2	1	0	0	0	30	20	50
6	BTME103	Workshop Practices	0	0	2	1	0	0	0	30	20	50
TOTAL			12	3	10	20	240	80	80	150	100	650

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Chujari
28/2/24

Neeloto
Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Neeloto
Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Hyman
Controller of Examinations
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Chap
Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

OK
Jac
28/2/24
Vice Chancellor
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore