




Shri Vaishnav Vidyapeeth Vishwavidyalaya
Shri Vaishnav Institute of Technology and Science
Choice Based Credit System (CBCS) Scheme in the light of NEP-2020
B.Tech. in Electrical/Electrical and Electronics Engineering

SEMESTER-IV (2022-2026)

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assess-ment* (20%)	End Sem University Exam (60%)	Teachers Assess-ment* (40%)	
1	BTEE401	Electrical Machines I	3	1	2	5	60	20	20	30	20	150
2	BTEE403	Power System I	3	0	2	4	60	20	20	30	20	150
3	BTEE404	Electromagnetic Field Theory	3	1	0	4	60	20	20	0	0	100
4	BTEI401	Microprocessor and Microcontroller	3	1	2	5	60	20	20	30	20	150
5	BTEE105	Basics of MATLAB	0	0	2	1	0	0	0	30	20	50
6	-	Generic Elective II	3	0	0	3	60	20	20	0	0	100
7	-	MOOCs I (Generic)	2	0	0	2	-	-	-	-	-	100
TOTAL			17	3	8	24	300	100	100	120	80	800

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical

***Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.**



Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore



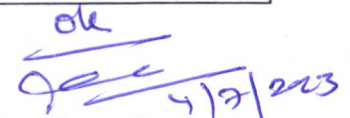
Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore



Controller of Examinations
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore



Joint Registrar
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Shri Vaishnav Vidyapeeth
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