	A REAL	Shri Vaishna										
	A State	Shri Vaishnav I				00						
	rest is attening	Choice Based Credit Syst	em (CB	CS) S	Schen	ie in	the light	of NI	EP-2020	)		
		B.Tech. in Elect	trical a	nd El	ectro	nics I	Engineeri	ng				
		SEM	ESTEI	<b>R-V</b> (	2021	-2025	)					
	COURSE		TEACHING SCHEME/WEEK			EXAMINATION SCHEME					SXD	
						ITS	THEORY			PRACTICAL		MARKS
S. No.	CODE	COURSE NAME	L	Т	Р	CREDITS	End Sem Unversity Exam (60%)	Two Term Exam (20%)	Teachers Assess- ment* (20%)	End Sem Unversity Exam (60%)	Teachers Assess- ment* (40%)	TOTAL M
1	BTEE501	Electrical Machines II	3	0	2	4	60	20	20	30	20	150
2	BTEE502	Power Electronics	2	1	2	4	60	20	20	30	20	150
3	BTEE503	Control System Engineering	2	1	2	4	60	20	20	30	20	150
4	BTEE504	Switchgear and Protection	2	1	2	4	60	20	20	30	20	150
5		Elective I	3	0	0	3	60	20	20	0	0	100
6	BTEE505	Minor Project	0	0	2	1	0	0	0	30	20	50
7	BTME510	Designing Thinking and Innovation	2	0	0	2	60	20	20	0	0	100
8		MOOCs II	2	0	0	2	-	-	-	-		100
Legend	s: L - Lecture	TOTAL ; T - Tutorial/Teacher Guided Student Acti	16 vity ; P - P	3 ractical	10	24	360	120	120	150	100	950
*Teach	er Assessment	shall be based on following components: Q mpulsorily opt MOOCS course of two cred	uiz/Assign	ment/Pr	oject/Pa	rticipat	tion in Class,	given tl	hat no com	ponent shal	l exceed m	iore

S. No.	<b>Course</b> Code	Elective I
1	BTEE511	Power Quality
2	BTEE512	Reliability Engineering
3	BTEE514	Introduction of IOT in Electrical Engineering

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Chairperson Board of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

. . Chairperson

Chairperson Faculty of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Controller of Examinations Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Joint Registrar Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

4.9.203

Vice Chancellor Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore