

Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Technology and Science

Choice Based Credit System (CBCS) Scheme in the light of NEP-2020

B.Tech. in Electronics and Communication (Internet of Things)

	T	· SEMES	ΓER	-VII	(202	1-202	5)	COLIM	mgsj		The Colombia State of the State of the Colombia State of the Colom	8
				EACH EME/	ING WEEK			EXAMI	NATION S	6CНЕМЕ		
S. No.	COURSE CODE	COURSE NAME				CREDITS		THEORY		PRAC	TICAL	
			L	Т	P	CRE	End Sem Universit y Exam (60%)	Two Term Exam (20%)	Teachers Assessme nt* (20%)	End Sem Universit y Exam (60%)	Teachers Assessme nt* (40%)	
- 1	BBAI501	Human Values and Professional Ethics	4	0	0	4	60	20	20	0	0	100
2	BTECIOT702	Security in IOT . **	3	0	0	3	60	20 .	20 5	0	0	100
3	-	Elective III	3	0	2	4	60	20	20	30	20	150
4	-	Elective IV .	3	1	0	4	60	. 20	20	0	0	100
5	·_	Generic Elective IV	3	0	0	3	60	20	20	0,	0	
6	BTEC703	System Verilog based Design and Verification Lab	0	0	4	2	0	0	0	30	20	100
7	BTECIOT705	Major Project	0	0	6	3	0	0	0	90	60	150
8	BTEEE701	Industrial Training	0	0	2	1	0	0	0	30	20	50
		TOTAL utorial/Teacher Guided Student Activity ; P - P	16	1	14	24	300	100	100	180	120	800

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10

S No	Course Code	Elective III
1	BTCS701	Cloud Computing
2	BTEC723	Advanced Microcontroller and Embedded Systems
3	BTECIOT701	Real Time Operating Systems

S No	Course Code	Elective IV
1	BTRA601	Advanced Control Systems
2	BTECIOT721	Energy Harvesting for IOT devices
3	BTECIOT703	Wireless Sensor Networks

Chairperson Board of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Chairperson Faculty of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Controller of Examinations

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Registrar Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Vice Chancellor

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore