

## Shri Vaishnav Vidyapeeth Vishwavidyalay. Shri Vaishnav Institute of Technology and Science Choice Based Credit System (CBCS) Scheme in the light of NEP-2020 B.Tech. in Electronics and Communication (Internet of Things)

SEMESTER-VI	(2021-2025)
	(

	SEMESTER-VI (2021-2025)											
	COURSE		TEACHING SCHEME/WEEK			EXAMINATION SCHEME				MARKS		
		COURSE NAME				CREDITS	THEORY		PRACTICAL		A R	
S. No.			L	Т	P		End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessme nt* (20%)	End Sem University Exam (60%)	Teachers Assessmen t* (40%)	TOTAL M.
1	ВТЕСІОТ601	Application Development for IOT	3	0	2	4	60	20	20	30	20	150
2	BTECIOT602	Data Analytics for IOT	3	0	2	4	60	20	20	30	20	150
3	BTEC603	Discrete Time Signal Processing	3	1	2	5	60	20	20	30	20	150
4	BTECIOT604	Embedded Systems with Raspberry pi and Arduino	0	0	2	1	0	0	0	30	20	50
5	-	Elective II	3	. 1	2	5	60	20	20	30	20	150
6	-	Generic Elective III	3	0	0	3	60	20	20	0	0	100
7	-	MOOCs III	0	0	0	2	-	-	-	-	50	50
8	BTEI404	PLC Lab	0	0	4	2	0	0	0	30	20	50
		TOTAL	15	2	14	26	300	100	100	180	170	850

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P - Practical;

<sup>\*</sup> Students have to compulsorily opt MOOCS course of two credit from Swayam Portal

S No	<b>Course Code</b>	Elective II
1	BTEC525	FPGA Based System Design
2	BTEI604	Digital Image Processing
3	BTEC714	Embedded Systems for Robotics

Chairperson

Board of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Chairperson

Faculty of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Controller of Examinations

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore Joint Registrar

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore 25/4/201

Vice Chancellor Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

<sup>\*</sup>Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.