



Shri Vaishnav Vidyapeeth Vishwavidyalaya
Shri Vaishnav Institute of Technology and Science
Choice Based Credit System (CBCS) Scheme in the light of NEP-2020
B.Tech. in Electronics and Communication

(For 2022-2026 Batch)

SEMESTER-V (2021-2025)

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S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teacher s Assessment* (40%)	
1	BTEI401	Microprocessor and Microcontroller	3	1	2	5	60	20	20	30	20	150
2	BTEC502	Cellular and Mobile Communication	3	0	0	3	60	20	20	0	0	100
3	BTEC504	CMOS VLSI Design	3	1	2	5	60	20	20	30	20	150
4	-	Elective I	3	1	2	5	60	20	20	30	20	150
5	-	MOOCs II	-	-	-	2	-	-	-	-	50	50
6	BTME510	Design Thinking and Innovation	2	0	0	2	60	20	20	0	0	100
7	BTEC506	Minor Project	0	0	4	2	0	0	0	30	20	50
8	BTEC507	Programming in Python	0	0	2	1	0	0	0	30	20	50
TOTAL			14	3	12	25	300	100	100	150	150	800
Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Project												

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more

* Students have to compulsorily opt MOOCs course of two credit from Swayam Portal

S. No.	Course Code	Elective I
1	BTEC525	FPGA Based System Design
2	BTEI611	Data Acquisition Systems
3	BTCS403	Data Structures and Algorithm

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 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya, Indore

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