



Shri Vaishnav Vidyapeeth Vishwavidyalaya
Shri Vaishnav Institute of Technology and Science
Choice Based Credit System (CBCS) Scheme in the light of NEP-2020
B.Tech. in Electrical Engineering
SEMESTER-III (2025-2029)

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	BTEC301	Advanced Programming Concepts	3	0	2	4	60	20	20	30	20	150
2	BTEC302	Network Analysis and Synthesis	3	1	2	5	60	20	20	30	20	150
3	BTEC304	Electronic Devices and Circuits	3	0	2	4	60	20	20	30	20	150
4	BTEE307	Electrical Instrumentation	3	0	2	4	60	20	20	30	20	150
5	-	Generic Elective I	4	0	0	4	60	20	20	0	0	100
6	BTEC308	Design Lab	0	0	2	1	0	0	0	30	20	50
TOTAL			16	1	10	22	300	100	100	150	100	750

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

6/7/2028

Chairperson
 Board of Studies
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya, Indore

Chairperson
 Faculty of Studies
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya, Indore

Controller of Examinations
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya, Indore

Registrar
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya, Indore

Vice Chancellor
 Shri Vaishnav Vidyapeeth
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