

Shri Vaishnav Vidyapeeth Vishwavidyalaya Shri Vaishnav Institute of Technology and Science Choice Based Credit System (CBCS) Scheme in the light of NEP-2020 B.Tech. in Electronics and Communication (Internet of Things)

	I	SEM			· · ·	021-2	2025)			-		
S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK				EXAMINATION SCHEME				S	
						CREDITS	THEORY			PRACTICAL		IAR
			L	Т	Р		End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assess- ment* (20%)	End Sem University Exam (60%)	Teachers Assess- ment* (40%)	TOTAL MARKS
1	BTEC302	Network Analysis and Synthesis	3	1	2	5	60	20	20	30	20	150
2	BTEC303	Electronic Measurement and Instrumentation	3	0	2	4	60	20	20	30	20	150
3	BTEC304	Electronic Devices and Circuits	3	1	2	5	60	20	20	30	20	150
4	BTECIOT301	Introduction to IOT and Embedded Systems	3	0	2	4	. 60	20	20	30	<u>2</u> 0	150
5	-	Generic Elective I	3	0	0	3	60	20	20	0	0	100
6	BTEC307	PCB Designing Lab	0	0	4	2	0	0	0	30	20	50
TOTAL			15	2	12	23	300	100	100	150	100	750
Legen	ds: L - Lecture ;	T - Tutorial/Teacher Guided Student Activity ;	P - Pr	actical	;	1	4	A management of the second		L	d	

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Chairperson Board of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

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Chairperson Faculty of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Controller of Examinations Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Joint Registrar Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

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Vice Chancellor Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore