



**Shri Vaishnav Vidyapeeth Vishwavidyalaya**  
**Shri Vaishnav Institute of Technology and Science**  
**Choice Based Credit System (CBCS) Scheme in the light of NEP-2020**  
**B.Tech. in Electronics and Communication (Internet of Things)**

**SEMESTER-IV (2021-2025)**

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	BTEC301	Advanced Programming Concepts	3	0	4	5	60	20	20	30	20	150
2	BTEE503	Control System Engineering	2	1	2	4	60	20	20	30	20	150
3	BTECIOT401	Sensors and Signal Conditioning	3	0	2	4	60	20	20	30	20	150
4	BTCS403	Data Structures and Algorithms	3	1	2	5	60	20	20	30	20	150
5	-	Generic Elective II	3	0	0	3	60	20	20	0	0	100
6	-	MOOCs I (Generic)	2	0	0	2	-	-	-	-	-	100
7	BTEC405	Programming with Arduino	0	0	4	2	0	0	0	30	20	50
<b>TOTAL</b>			<b>16</b>	<b>2</b>	<b>14</b>	<b>25</b>	<b>300</b>	<b>100</b>	<b>100</b>	<b>150</b>	<b>100</b>	<b>850</b>

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

\*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

\* Students have to compulsorily opt MOOCs course of two credit from Swayam Portal

**Chairperson**  
Board of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

**Chairperson**  
Faculty of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

**Controller of Examinations**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

**Joint Registrar**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

  
7/2/2022

**Vice Chancellor**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore