

# Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)

Choice Based Credit System (CBCS) Scheme

**Bachelor of Technology (Electronics & Communication)**

IV - SEMESTER(w.e.f. Batch 2018-19)

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Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Assessment*	End Sem University Exam	Teachers Assessment*		
1	BTEC401	Linear Integrated Circuits		3	1	2	60	20	20	30	20	150	5
2	BTEC402	Digital Electronics		2	1	2	60	20	20	30	20	150	4
3	BTEC403	Electro Magnetic Theory		3	1	0	60	20	20	0	0	100	4
4	BTEC404	Analog Communication		3	0	2	60	20	20	30	20	150	4
5	BTCS403	Data Structures and Algorithms		3	1	2	60	20	20	30	20	150	5
6	BTEC405	Programming with Arduino		0	0	4	0	0	0	60	40	100	2
TOTAL				14	4	12	300	100	100	180	120	800	24
*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.													
* Students are advised to take online MOOCS course of minimum two credits													

  
**Chairperson**  
**Board of Studies**  
 Shri Vaishnav Vidyapeeth Vishwavidyalaya

  
**Registrar**  
 Shri Vaishnav Vidyapeeth Vishwavidyalaya  
 INDORE (M.P.)