

Shri Vaishnav Vidyapeeth Vishwavidyalaya Shri Vaishnav Institute of Technology and Science Choice Based Credit System (CBCS) Scheme in the light of NEP-2020 B.Tech. in Electrical and Electronics Engineering

SEMESTER-IV (2021-2025)

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK				EXAMINATION SCHEME					KS
						CREDITS	THEORY			PRACTICAL		MARKS
			L	Т	Р		End Sem Unversity Exam (60%)	Two Term Exam (20%)	Teachers Assess- ment* (20%)	End Sem Unversity Exam (60%)	Teachers Assess- ment* (40%)	TOTAL
1	BTEE401	Electrical Machines I	3	1	2	5	60	20	20	30	20	150
2	BTEC402	Digital Electronics	2	1	2	4	60	20	20	30	20	150
3	BTEE403	Power System I	3	0	2	4	60	20	20	30	20	150
4	BTEI401	Microprocessor and Microcontroller	3	1	2	5	60	20	20	30	20	150
5	BTEC507	Programming in Python	0	0	2	1	0	0	0	30	20	50
6	-	Generic Elective II	3	0	0	3	60	20	20	0	0	100
7		MOOCs I (Generic)	2	0	.0	2	-	-	-	-	-	100
TOTAL				3	10	24	300	100	100	150	100	850
		re; T - Tutorial/Teacher Guided Studen										

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

* Students have to compulsorily opt MOOCS course of two credit from Swayam Portal

Chairperson Board of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Chairperson Faculty of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Controller of Examinations Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Joint Registrar Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

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Vice Chancellor Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore