## Shri Vaish av Vidyapeeth Vishwavidyala a, Indore (MP)

**Choice Based Credit System (CBCS) Scheme** 

## B.Tech in Electronics and Communication with Specialization in IOT

			V - SEM	ESTE	R(w.e.f	. Batcl	n 2018-19)			57 			
Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme						
					т	Ρ	Theory			Practical		Total	
				L			End Sem Unversity Exam	Two Term Exam	Teachers Asses- ment*	End Sem Unversity Exam		Marks	Credits
1	BTEC501	Microprocessor, Microcontroller and Interfacing		3	1	2	60	20	20	30	20	150	5
2	BTCS405	Data Base Management System		3	1	2	60	20	20	30	20	150	5
3	BTECIOT501	Communication Systems		3	0	2	60	20	20	30	20	150	4
4	BTECIOT502	Wirelesss and Mobile Communication		3	0	0	60	20	20	0	0	100	3
5	BTEI611	Elective: 1 CMOS VLSI Design 2 Data Communication and Computer Networks 3 Data Acquisition System		3	1	2	60	20	20	30	20	150	5
6	BTECIOT503	Minor Project		0	0	2	0	0	0	30	20	50	1
7	BTIT406	Unix and Shell Programming Lab		0	0	2	0	0	0	0	50	50	1
TOTAL				15	3	12	300	100	100	150	150	800	24
exc	eed more that	sment shall be based on following co an 10 marks. dvised to take online MOOCS course					Project/Pat	icipati	on in Clas	s, given th	at no co	mpone	nt shall

\* Students are advised to take online MOOCS course of minimum two credits

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Chairperson Faculty of Studies Shri Vaishnav Vidyapeeth Vishwavidyalaya, INDORE Controller of Examinations Shri Vaishnav Vidyapeeth Vishwavidyalay

Registrar Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Vice Chancellor Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Chairperson Vishw Board of Studies Indore