



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Bachelor of Technology (Electrical Engineering)

Choice Based Credit System (CBCS)

		SEM	EST	ER	- VI	[(w.e.	.f. Batch	2018-19))(20)	018 - 2	1022)	
S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK				EXAMINATION SCHEME					KS
			L	т	Р	CREDITS	THEORY			PRACTICAL		MARKS
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	TOTAL
1	BTEE701	Electric Drives	2	1	2	4	60	20	20	30	20	150
2	BTEE702	Electrical Machine Design	2	1	2	4	60	20	20	30	20	150
3	BTEE703	Major Project	0	0	8	4	0	0	0	60	40	100
4	BTEE704	Software Simulation Lab	0	0	2	1	0	0	0	30	20	50
5	BTEE705	Electrical Design and Drawing	0	0	2	1	0	0	0	0	50	50
6		Elective-I	3	0	0	3	60	20	20	0	0	100
7		Elective-II	3	0	0	3	60	20	20	0	0	100
8	BBAI501	Human Values and Professional Ethics	4	0	0	4	60	20	20	0	0	100

24 Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

14

2

16

	Elective-I	Elective-II			
BTEE711	High Voltage Engineering	BTEE721	Digital Signal Processing		
BTEE712	Extra High Voltage Alternating Current and Direct Current Transmission	BTEE722	Supervisory Control and Data Acquisition systems and Applications		
BTEE713	Power Plant Instrumentation and Control	BTEE723	Artificial Neural Network		

300

100

100

150

150

800

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks. Note: Students are advised to take online MOOCS courses of minimum 2 credits

Accent

Chairperson Faculty of Studies Shri Vaishnav Vidyapeeti. Vishwavidyalaya, INDORE

Controller of Examinations Shri Vaishnev Vidyapeeth Vishwavidyalay

Registra

Vice Chancellor

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Chairperson Board of Studies Shri Vaishnav Vidyapeeth Vishwavidyalava Indere