

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Shri Vaishnav Institute of Technology & Science, Indore

Choice Based Credit System (CBCS) Scheme

B Tech. (Electronics and Instrumentation)

V - SEMESTER

				Teaching Scheme/week			Examination Scheme						
S. No.	Subject Code	Name of Subject	Course Code				Theory			Practical		Total	
				L	Т	P	End Sem University Exam	Two Term Exam	Teachers Assessment*	End Sem University Exam	Teachers Assessment*	Marks	
1	BTEI501	Digital Signal Processing		3	0	2	60	20	20	30	20	150	4
2	BTEI502	Fiber Optic Instrumentation		3	0	0	60	20	20	0	0	100	3
3	BTEE503	Control System Engineering		3	1	2	60	20	20	30	20	150	5
4	BTEC504	CMOS VLSI Design		3	1	2	60	20	20	30	20	150	5
5	BTCS403 BTE 1515 BTMT511 BTE 1511	Elective		3	1	2	60	20	20	30	20	150	5
6	BTE1503	Labview		0	0	2	0	0	0	30	20	50	1
7	BTE1504	Minor Project		0	0	2	0	0	0	30	20	50	1
OTAL				15	3	12	300	100	100	180	120	800	24

^{*}Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

*Students are advised to take online moocs courses of 2 credits.

Elective	Data Structure & Algorithms(BTCS 403)	3	1	2	60	20	20	30	20	150	5
	Data Communication (BTE\$515)	3	1	2	60	20	20	30	20	150	5
	Automation(BTMT511)	3	1	2	60	20	20	30	20	150	5
	Instrumentation system design (BTEI511)	3	1	2	60	20	20	30	20	150	5

Chairperson

Board of Studies

Controller of Examinations Shri Vaishnav Vidyapeeth Vishwavidyalay

Registrar Shri Vaishnav Vidyapeeth

Vice Chancellor Shri Vaishnav Vidyapeeth Victorial alar la lare

Shri Vaishnav Vidyapeeth Vishwavidyalaya