



Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Shri Vaishnav Institute of Technology & Science, Indore

Choice Based Credit System (CBCS) Scheme

Diploma in Electronics and Instrumentation Engineering

Session 2018-2021

III-SEMESTER														
S. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/week			Examination Scheme						Total Marks	CREDITS
				L	T	P	Theory			Practical				
							End Sem University Exam	Two Term Exam	Teachers Assessment**	End Sem University Exam	Teachers Assessment*			
1	DTEI301	Measurement & Instrumentation		2	1	2	60	20	20	30	20	150	4	
2	DTEI302	Digital Electronics		2	1	2	60	20	20	30	20	150	4	
3	DTEI303	Analog Electronics		2	1	2	60	20	20	30	20	150	4	
4	DTEI304	Electro Magnetic Theory		2	1	0	60	20	20	0	0	100	3	
5	DTEI305	PLC Lab		0	0	4	0	0	0	30	20	50	2	
6	DTEI306	Electronics workshop		0	0	4	0	0	0	30	20	50	2	
7	DTEI307	Network Analysis		3	0	0	60	20	20	0	0	100	3	
TOTAL				11	4	14	300	100	100	150	100	750	22	

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore

Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore