


Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)													
Choice Based Credit System (CBCS) Scheme													
Bachelor of Technology (Electronics & Communication)													
II - SEMESTER (w.e.f. Batch 2018-19)													
Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Asses- ment*	End Sem University Exam	Teacher s Asses- ment*		
1	BTPH101	Applied Physics		3	1	2	60	20	20	30	20	150	5
2	BTMA201	Applied Mathematics-II		3	1	0	60	20	20	0	0	100	4
3	BTCE103	Applied Mechanics		2	1	2	60	20	20	30	20	150	4
4	BTEC101	Introduction to Communication Engineering		3	1	2	60	20	20	30	20	150	5
5	BTME102	Fundamentals of Mechanical Engineering		3	0	2	60	20	20	30	20	150	4
6	BTEC103	Electronics Workshop		0	0	2	0	0	0	30	20	50	1
7	BTME103	Workshop Practices		0	0	2	0	0	0	30	20	50	1
TOTAL				14	4	12	300	100	100	180	120	800	24
*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.													
* Students are advised to take online MOOCS course of minimum two credits													



**Chairperson  
Board of Studies**

Shri Vaishnav Vidyapeeth Vishwavidyalaya  
Indore

  
**Registrar**

Shri Vaishnav Vidyapeeth Vishwavidyalaya  
INDORE (M.P.)