



Shri Vaishnav Vidyaapeeth Vishwavidyalaya, Indore

Shri Vaishnav Institute of Technology & Science, Indore

Choice Based Credit System (CBCS) Scheme

B. Tech. (Electronics and Instrumentation)


W.E.F. 2018-19

III - SEMESTER

S. No.	Subject Code	Name of Subject	Course Code	TEACHING			Examination Scheme						TOTAL MARKS
				Th	T	P	Theory			Practical			
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)		
1	BTEC402	Digital Electronics		2	1	2	60	20	20	30	20	150	
2	BTEI302	Measurement and Instrumentation		2	1	2	60	20	20	30	20	150	
3	BTEC302	Network Analysis and Synthesis		3	1	2	60	20	20	30	20	150	
4	BTEC305	Electronic Circuits		3	1	2	60	20	20	30	20	150	
5	ML301	Environment and Energy Studies		4	0	0	60	20	20	0	0	100	
6	BTECS207	Computer Programming-II		0	0	2	0	0	0	30	20	50	
7	BTMT302	PCB Designing Lab		0	0	2	0	0	0	30	20	50	
TOTAL				14	4	12	300	100	100	180	120	800	

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

*Students are advised to take online moocs courses of 2 credits.


Chairperson
Board of Studies
Shri Vaishnav Vidyaapeeth Vishwavidyalaya
Indore


Registrar
Shri Vaishnav Vidyaapeeth Vishwavidyalaya
INDORE (M.P.)