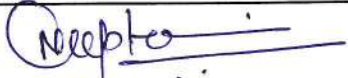


Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)													
Choice Based Credit System (CBCS) Scheme													
Bachelor of Technology (Electronics & Communication)													
I - SEMESTER (w.e.f. Batch 2018-19)													
Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Assessment*	End Sem University Exam	Teacher s Asses- ment*		
1	BTPH101	Applied Physics		3	1	2	60	20	20	30	20	150	5
2	BTMA101	Applied Mathematics-I		3	1	0	60	20	20	0	0	100	4
3	BTEE102	Fundamentals of Electrical Engineering		3	0	2	60	20	20	30	20	150	4
4	BTEC102	Fundamentals of Electronics Engineering		3	0	2	60	20	20	30	20	150	4
5	BTME101	Engineering Drawing		2	0	4	60	20	20	30	20	150	4
6	HUCS101	Communication Skills		1	0	2	60	20	20	0	20	120	2
7	BTCS101	Computer Programming-I		0	0	2	0	0	0	30	20	50	1
TOTAL				15	2	14	360	120	120	150	120	870	24
*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.													
* Students are advised to take online MOOCS course of minimum two credits													


Chairperson
Board of Studies
 Shri Vaishnav Vidyapeeth Vishwavidyalaya
 Indore


Registrar
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya, Indore