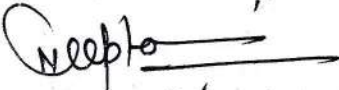


Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)													
Choice Based Credit System (CBCS) Scheme													
M.Tech. in Embedded System													
III - SEMESTER													
Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Asses-ment*	End Sem University Exam	Teachers Asses-ment*		
1	MBAI202	Research Methodology		3	1	0	60	20	20	0	0	100	4
2	MBAI301C	Human Values and Professional Ethics		3	1	0	60	20	20	0	0	100	4
3	MTES301	Dissertation Part I		0	0	24	0	0	0	180	120	300	12
TOTAL				6	2	24	120	40	40	180	120	500	20
*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.													

  
**Chairperson**  
 Board of Studies  
 Shri Vaishnav Vidyapeeth Vishwavidyalaya  
 Indore

  
**Registrar**  
 Shri Vaishnav Vidyapeeth Vishwavidyalaya  
 INDORE (M.P.)