

# Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)

Choice Based Credit System (CBCS) Scheme

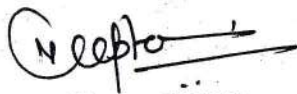
**M.Tech. in Embedded System**

**IV - SEMESTER**

Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Asses- ment*	End Sem University Exam	Teachers Asses- ment*		
1	MTES411	Dissertation Part II		0	0	40	0	0	0	300	200	500	20
2	MTES421	Industrial Internship		0	0	40	0	0	0	300	200	500	20
TOTAL				0	0	40	0	0	0	300	200	500	20

\*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

\*\* Either Student has to undergo Industrial Internship or opt Dissertation Part II.

  
**Chairperson**  
**Board of Studies**  
 Shri Vaishnav Vidyapeeth Vishwavidyalaya  
 Indore

  
**Registrar**  
 Shri Vaishnav Vidyapeeth Vishwavidyalaya  
 INDORE (M.P.)