

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)

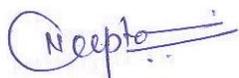
Choice Based Credit System (CBCS) Scheme

M.Tech Digital Communication

I - SEMESTER

Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Assessment*	End Sem University Exam	Teachers Assessment*		
1	MTMA101	Advanced Mathematics		3	1	0	60	20	20	0	0	100	4
2	MTDC101	Data Communication & Computer Networks		3	1	2	60	20	20	30	20	150	5
3	MTDC102	Advanced Digital Communication		3	1	2	60	20	20	30	20	150	5
4	MTDC113 123/133	1 Probability & Random Process 2 Embedded Systems 3 Mobile & Satellite Communication		3	1	0	60	20	20	0	0	100	4
5	MTDC114 /124/134	Elective: 1 Information Theory & Coding 2 CMOS VLSI Design 3 Optical Communication		3	1	0	60	20	20	0	0	100	4
6	MTDC105	LINUX & C Programming		0	0	2	0	0	0	30	20	50	1
7	MTDC106	MATLAB for Communication System		0	0	2	0	0	0	30	20	50	1
8	MTDC107	Comprehensive Viva		0	0	0	0	0	0	0	50	50	4
9	MTDC108	Seminar		0	0	4	0	0	0	0	50	50	2
TOTAL				15	5	12	300	100	100	120	180	800	30

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.



Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore



Deputy Registrar
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore