



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme

Bachelor of Technology (CSE with Specialization in Information and Cyber Security)

SEMESTER- I (2021-2025)

SUBJECT CODE	Category	SUBJECT NAME	TEACHING & EVALUATION SCHEME								
			THEORY			PRACTICAL		Th	T	P	CREDITS
			END SEM University Exam	Two Term Exam	Teachers Assessment*	END SEM University Exam	Teachers Assessment*				
BTMACS101	BS	Mathematics-I	60	20	20	0	0	3	1	0	4

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P – Practical; C - Credit;
*Teacher Assessment shall be based following components: Quiz/Assignment/
Project/Participation in Class, given that no component shall exceed more than 10 marks.

Course Educational Objectives (CEOs):

1. The student will have ability to:
2. 1. To introduce the students with the Fundamentals of the Differential, Integral, Vector Calculus and Numerical Analysis.

Course Outcomes:

1. Upon completion of the subject, students will be able to:
2. Understand and apply the basics of the differential calculus.
3. Know the fundamental principles of the integral calculus and apply them.
4. Apply the techniques in the numerical analysis.
5. Know the numerical solution of the system of linear algebraic equations.
6. Understand and apply the basics of the vector calculus.

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Controller of Examination
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme
Bachelor of Technology (CSE with Specialization in Information and Cyber Security)
SEMESTER- I (2021-2025)

Syllabus:

UNIT I 10 HRS

Differential Calculus : Limits of functions, continuous functions, uniform continuity, monotone and inverse functions. Differentiable functions, Rolle's theorem, mean value theorems and Taylor's theorem, power series. Functions of several variables, partial derivatives, chain rule, Tangent planes and normals. Maxima, minima, saddle points, Lagrange multipliers, exact differentials

UNIT II 9 HRS

Integral Calculus : Riemann integration, fundamental theorem of integral calculus, improper integrals. Application to length, area, volume, surface area of revolution. Multiple integrals with application to volume, surface area, Change of variables.

UNIT III 8 HRS

Numerical Analysis : Number Representation and Errors: Numerical Errors; Floating Point Representation; Finite Single and Double Precision Differences; Machine Epsilon; Significant Digits.

Numerical Methods for Solving Nonlinear Equations: Method of Bisection, Secant Method, False Position, Newton-Raphson's Method, Multidimensional Newton's Method, Fixed Point Method and their convergence.

UNIT IV 7 HRS

Numerical Methods for Solving System of Linear Equations: Norms; Condition Numbers, Forward Gaussian Elimination and Backward Substitution; Gauss-Jordan Elimination; FGE with Partial Pivoting and Row Scaling; LU Decomposition; Iterative Methods: Jacobi, Gauss Seidel; Power method and QR method for Eigen Value and Eigenvector.

UNIT V 8 HRS

Vector Calculus : Gradient and directional derivative. Divergence and Curl of Vector point function, line and surface integrals. Green's, Gauss' and Stokes' theorems and their applications.

Chairperson	Chairperson	Controller of Examination	Joint Registrar
Board of Studies	Faculty of Studies	Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth
Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth	Vishwavidyalaya, Indore	Vishwavidyalaya, Indore
Vishwavidyalaya, Indore	Vishwavidyalaya, Indore		



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme
Bachelor of Technology (CSE with Specialization in Information and Cyber Security)
SEMESTER- I (2021-2025)

TEXT BOOKS:

1. T. M. Apostol, Calculus, Volume I, 2nd Ed, Wiley, 1967.
2. T. M. Apostol, Calculus, Volume II, 2nd Ed, Wiley, 1969.
3. K. E. Atkinson, Numerical Analysis, John Wiley, Low Price Edition(2004).
4. S. D. Conte and C. de Boor, Elementary Numerical Analysis - An Algorithmic Approach, McGraw-Hill, 2005.
5. B. S. Grewal, Higher Engineering Mathematics, Khanna Publishers, Delhi

REFERENCES:-

1. R. G. Bartle and D. R. Sherbert, Introduction to Real Analysis, 5th Ed, Wiley, 1999.
2. J. Stewart, Calculus: Early Transcendentals, 5th Ed, Thomas Learning (Brooks/ Cole), Indian Reprint, 2003.
3. J. Stewart, Calculus: Early Transcendentals, 5th Ed, Thomas Learning (Brooks/ Cole), Indian Reprint, 2003.
4. J. D. Hoffman, Numerical Methods for Engineers and Scientists, McGraw-Hill, 2001.
5. M.K Jain, S.R.K Iyengar and R.K Jain, Numerical methods for scientific and engineering computation (Fourth Edition), New Age International (P) Limited, New Delhi, 2004.
6. S. C. Chapra, Applied Numerical Methods with MATLAB for Engineers and Scientists, McGraw-Hill 2008.

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Controller of Examination
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme
Bachelor of Technology (CSE with Specialization in Information and Cyber Security)
SEMESTER- I (2021-2025)

SUBJECT CODE	Category	SUBJECT NAME	TEACHING & EVALUATION SCHEME								
			THEORY			PRACTICAL		Th	T	P	CREDITS
			END SEM University Exam	Two Term Exam	Teachers Assessment*	END SEM University Exam	Teachers Assessment*				
BTPH101	BS	Applied Physics	60	20	20	30	20	3	1	2	5

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P – Practical; C – Credit.

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Course Educational Objectives (CEOs):

1. To develop the comprehensive understanding of laws of physics.
2. To develop ability to apply laws of physics for various engineering applications.
3. To develop the experimental skills, ability to analyze the data obtained experimentally to reach substantiated conclusions.

Course Outcomes (COs):

1. Student will be able to comprehend laws of physics.
2. Student will be able to apply laws of physics for various engineering applications.
3. Student will be able to determine physical parameter experimentally and will be able to analyze the data obtained experimentally to draw substantiate conclusions.

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Controller of Examination
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme
Bachelor of Technology (CSE with Specialization in Information and Cyber Security)
SEMESTER- I (2021-2025)

UNIT I

10 HRS

Quantum Physics: Introduction to Quantum hypothesis, Matter wave concept, Wave Group and Particle velocity and their relations, Uncertainty principle with elementary proof and applications to microscope and single slit, Compton Effect, Wave function and its physical significance. Development of time dependent and time independent Schrodinger wave equation, Applications of time independent Schrodinger wave equation.

UNIT II

9 HRS

Solid State Physics: Free electron model, Qualitative Analysis of Kronig Penney Model, Effective mass, Fermi level for Intrinsic and Extrinsic semiconductors, P-N junction diode, Zener diode, Tunnel diode, Photodiode, Solar- cells, Hall Effect, Introduction to Superconductivity, Meissner effect, Type I & II Superconductors

UNIT III

8 HRS

Nuclear Physics: Nuclear Structure & Properties Nuclear models: Liquid drop with semi-empirical mass formula & shell model. Particle accelerators: Cyclotron, Synchrotron, Betatron. Counters and Detectors: Giger-Muller counters, Bainbridge Mass Spectrograph and Auston Mass Spectrograph.

UNIT IV

7 HRS

Laser & Fiber Optics: Stimulated and Spontaneous Emission, Einstein's A&B Coefficients, Population Inversion, Pumping, Techniques of Pumping, Optical Resonator, Properties and Applications of Laser, Ruby, Nd:YAG, He-Ne lasers. Introduction to Optical fibre, Acceptance angle and cone, Numerical Aperture, V- Number, Ray theory of propagation through optical fibre, Pulse dispersion , applications of optical fibre.

UNIT V

8 HRS

Wave Optics: Introduction to Interference, Fresnel's Bi-prism, Interference in Thin films, Newton's rings experiment, Michelson's interferometer and its application, Introduction to Diffraction and its Types, Diffraction at single slit, double slit, resolving power, Rayleigh criterion, Resolving power of grating, Concept of polarized light, Double refraction, quarter and half wave plate, circularly & elliptically polarized light.

Chairperson	Chairperson	Controller of Examination	Joint Registrar
Board of Studies	Faculty of Studies	Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth
Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth	Vishwavidyalaya,Indore	Vishwavidyalaya,Indore
Vishwavidyalaya,Indore	Vishwavidyalaya,Indore		



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme
Bachelor of Technology (CSE with Specialization in Information and Cyber Security)
SEMESTER- I (2021-2025)

TEXTBOOKS:-

1. Engineering Physics by Dr. S. L. Gupta and Sanjeev Gupta, DhanpatRai Publication, NewDelhi.
2. Engineering Physics by Navneet Gupta, DhanpatRai Publication, NewDelhi.
3. Engineering Physics by H. J. Sawant, Technical Publications, Pune, Maharashtra.
4. Engg Physics by M.N. Avdhanulu & P.G. Kshirsagar, S.Chand & Co. Edition (2010).
5. Fundamentals of Physics by Halliday, Wiley, India.

REFERENCES:-

1. Concepts of Modern Physics by Beiser, TMH, NewDelhi.
2. Solid State Physics by Kittel, Wiley India.
3. Atomic and Nuclear physics by Brijlal and Subraminiyan.
4. LASERS and Electro Optics by Christopher C. Davis, Cambridge Univ. Press (1996).
5. Optoelectronics an Introduction by J. Wilson & J.F.B. Hawkes, "" Prentice-Hall II Edition.
6. LASER theory and applications by A. K. Ghatak & Tyagarajan, TMH (1984). Optics by Ghatak, TMH.

PRACTICAL LIST:-

1. Measurement of radius of curvature "R" of convex lens by Newton's ring experiment.
2. Measurement of Numerical aperture of fiber by LASER.
3. Determination of Energy band gap „Eg“ of Ge using Four Probe method.
4. Measurement of Frequency of A.C. mains by electrically maintained vibrating rod.
5. Measurement of Resolving Power of Telescope.
6. Measurement of "λ" of LASER light source using Diffraction Grating.
7. Determination of Planck's constant by using photocell.
8. Determination of Energy band gap (Eg) using PN Junction Diode.
9. To determine the mass of cane sugar dissolved in water using half shade polarimeter.
10. To study forward and reverse characteristics of Zener diode.
11. To study forward and reverse characteristics of P-N diode.
12. To study characteristics of Photodiode.
13. To study characteristics of LDR.
14. μ and ω of given prism using spectrometer.
15. Measuring height of a given object using Sextant.

Chairperson	Chairperson	Controller of Examination	Joint Registrar
Board of Studies	Faculty of Studies	Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth
Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth	Vishwavidyalaya, Indore	Vishwavidyalaya, Indore
Vishwavidyalaya, Indore	Vishwavidyalaya, Indore		



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme

Bachelor of Technology (CSE with Specialization in Information and Cyber Security)

SEMESTER- I (2021-2025)

SUBJECT CODE	Category	SUBJECT NAME	TEACHING & EVALUATION SCHEME								
			THEORY			PRACTICAL		Th	T	P	CREDITS
			END SEM University Exam	Two Term Exam	Teachers Assessment*	END SEM University Exam	Teachers Assessment*				
BTEC104	DC	Digital Logic & Circuit Design	60	20	20	30	20	3	1	2	5

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P – Practical; C - Credit;
*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Course Educational Objectives (CEOs):

The objective of this course is to:

1. Use of Boolean algebra and Karnaugh Map to simplify logic function.
2. Describe the operation of different Combinational and Sequential Logic Circuits.

Course Outcomes (COs):

After completion of this course the students are expected to be able to demonstrate following knowledge, skills and attitudes.

The students will be able to

1. Design an optimal digital logic circuit to meet the given specifications.
2. Evaluate the performance of the given digital logic circuit based on specific criteria for reliable system implementation.

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Controller of Examination
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme
Bachelor of Technology (CSE with Specialization in Information and Cyber Security)
SEMESTER- I (2021-2025)

Syllabus:

UNIT I 10HRS

Number System & Codes: Introduction to number systems, Binary numbers, Octal & Hexadecimal Numbers, Number base Conversion, Signed binary numbers : 1's Complement & 2's Complement representation and their arithmetic operation, Floating point representation, binary codes, BCD, ASCII, EBCDIC, Gray codes, Error detecting and Correcting codes, Hamming codes.

UNIT II 9HRS

Boolean algebra and Logic gates: Introduction, Logic operations, Axioms and laws of Boolean algebra, Demorgan's theorem, Boolean functions, Canonical and standard forms. Logic gates and their applications, universal gates, NAND-NOR implementation of logic functions. Minimization techniques for logic functions-K-map, Tabular / QuineMcCluskey method.

UNIT III 8HRS

Combinational logic: Arithmetic circuits- Half adder, Full adder, Halfsubtractor, Full subtractor, Parallel and Serial adder, BCD adder, Multiplexer, De-multiplexer, Encoder & Decoder.

UNIT IV 7HRS

Sequential logic: Introduction, Latch and Flip Flop- S-R, D, JK and T, State diagram, characteristic equation, state table and excitation table, Flip flop conversion, applications of Flip flop, Counters, Registers.

UNIT V 8HRS

Semiconductor Memories and A/D and D/A converters: Semiconductor Memory – RAM, ROM- Organization, operation and their Types, PLD- PAL, PLA, PROM, FPGA, Analog to Digital (A/D) and Digital to Analog (D/A) converters and their types.

Chairperson	Chairperson	Controller of Examination	Joint Registrar
Board of Studies	Faculty of Studies	Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth
Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth	Vishwavidyalaya, Indore	Vishwavidyalaya, Indore
Vishwavidyalaya, Indore	Vishwavidyalaya, Indore		



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme
Bachelor of Technology (CSE with Specialization in Information and Cyber Security)
SEMESTER- I (2021-2025)

TEXT BOOKS:

- 1.M. Morris Mano,“Digital Logic and Computer Design”, Pearson Education, 2016.
- 2.S Salivahanan and S Arivazhagan: Digital Circuits and Design,4th Edition, Vikas Publishing House, 2012.

REFERENCE BOOKS:

1. A. Anand Kumar, “Fundamentals of Digital Circuits”, 4th Edition, PHI,2016.
2. Floyd and Jain,“Digital Fundamentals”, 10th Edition, Pearson Education India,2011.
3. Roland J.Tocci,Widmer,Moss, “Digital Systems Principles and Applications”, 10th Edition, Pearson 2009.
4. Stephen Brown, ZvankoVranesic, “Fundamentals of Digital Logic Design”, 3rd Edition, McGraw Hill, 2017.

LIST OF PRACTICAL’S:

- 1.To study and test of operation of all logic gates for various IC’s(IC7400,IC7403,IC408,IC74332,IC7486).
2. Verification of DeMorgan’s theorem.
3. To construct of half adder and fulladder.
4. To construct of half subtractor and full subtractor circuits.
5. Verification of versatility of NAND gate.
6. Verification of versatility of NOR gate.
7. Design a BCD to excess 3codeconverter.
8. Design a Multiplexer/Demultiplexer
9. Analysis of various flip flops with Preset and Clear capability.
10. Design of Johnson and Ring counter.
11. Design of synchronous and asynchronous up/down counters.

Chairperson	Chairperson	Controller of Examination	Joint Registrar
Board of Studies	Faculty of Studies	Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth
Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth	Vishwavidyalaya,Indore	Vishwavidyalaya,Indore
Vishwavidyalaya,Indore	Vishwavidyalaya,Indore		



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme
Bachelor of Technology (CSE with Specialization in Information and Cyber Security)
SEMESTER- I (2021-2025)

SUBJECT CODE	Category	SUBJECT NAME	TEACHING & EVALUATION SCHEME								
			THEORY			PRACTICAL		Th	T	P	CRED ITS
			END SEM University Exam	Two Term Exam	Teachers Assessment*	END SEM University Exam	Teachers Assessment*				
BTCS101N	DC	Introduction to Computer Science and Engineering	60	20	20	0	50	2	0	2	3

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P – Practical; C - Credit;

***Teacher Assessment** shall be based following components: Quiz/Assignment/ Project/Participation in Class, given that no component shall exceed more than 10 marks.

Course Educational Objectives (CEOs):

1. To introduce the fundamentals concepts of Computer system.
2. To introduce about history of Computer.
3. Understanding the basic concepts and features of various kinds of Operating systems.
4. Learning the Concepts of Office Automation Tools.

Course Outcomes (COs):

The students will be able to

1. Understand the basic terminologies of Computer System.
2. Gain knowledge about various kinds of Operating Systems and their features.
3. Learn the Concepts of Office Automation Tools.

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Controller of Examination
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme

Bachelor of Technology (CSE with Specialization in Information and Cyber Security)

SEMESTER- I (2021-2025)

Syllabus

Unit-I

10HRS

Introduction to Computer Fundamentals: Introduction to Computer, Objectives, Hardware and Software, Block Diagram of the Computer, Functions of the different Units, Applications of Computers Representation of data and information, Computer Languages, Machine language, Assembly Language, High level Language, Number System and Conversion, Classification and History of Computers, Introduction to Free and Open Source Software, Computer Virus, Use of Antivirus software.

Unit-II

8HRS

The Operating System: The Graphical User Interface (GUI), Definition of Operating System, Objective, Types and Functions of Operating Systems, Windows Operating System, Installing MS Windows, Working with Windows Operating System, System Tools and Applications in Windows, MS-DOS, Basic DOS commands, Comparison of DOS and Windows, case study of Unix, Linux OS.

Unit-III

9HRS

Office Automation Tools-I: Word Processing Basics, Elements of word Processing and Working Objectives, MS-Word Screen and its Components ,MS-Office ,Working with MS-Word, Menu Bar, Creating Documents, Using Templates, Saving a documents, Working with documents, Setting up pages of a document, Printing Documents with different options, Using Tables and Columns, Object Linking and Embedding, Hyperlink, Envelopes & Label Creation, Grammar & Spell Check, Mail Merge, Macro Creation, Previewing and Printing Documents.

Unit-IV

7HRS

Office Automation Tools-II Spread Sheet: Introduction to MS-Excel, Starting MS-Excel, Basics of Workbook and Spreadsheet, MS-Excel Screen and Its Components, Features of Excel, Elementary Working with MS-Excel, Manipulation of cells, Formatting of Spreadsheet and Cells, Formulas and Functions, Spread sheets for Small accountings, Previewing and Printing a Worksheet.

Unit-V

8HRS

Office Automation Tools-III Power-point: Introduction to MS-PowerPoint, Basics of PowerPoint, MS-PowerPoint Screen and Its Components, Features of PowerPoint, Elementary Working with MS-PowerPoint, Preparation of Slides, Creation of Presentation, Providing aesthetics, Slide Manipulation and Slide Show, Presentation of the Slides.

Chairperson	Chairperson	Controller of Examination	Joint Registrar
Board of Studies	Faculty of Studies	Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth
Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth	Vishwavidyalaya,Indore	Vishwavidyalaya,Indore
Vishwavidyalaya,Indore	Vishwavidyalaya,Indore		



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme
Bachelor of Technology (CSE with Specialization in Information and Cyber Security)
SEMESTER- I (2021-2025)

Text Books:

1. E Balagurusamy , “Fundamentals of Computers ”,TMH .
2. Silakari and Shukla, “Basic Computer Engineering ”, Wiley India.
3. V. Rajaraman, Neeharika Adabala, “Fundamentals of Computers”, PHI
4. Ajoy Kumar Ray and Tinku Acharya ,“ Basic Computer Engineering”, PHI.
5. P K Sinha ,“Fundamentals of Computers ” , BPB Publications.

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Controller of Examination
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme
Bachelor of Technology (CSE with Specialization in Information and Cyber Security)

SEMESTER- I (2021-2025)

SUBJECT CODE	Category	SUBJECT NAME	TEACHING & EVALUATION SCHEME								
			THEORY			PRACTICAL		Th	T	P	CRED ITS
			END SEM University Exam	Two Term Exam	Teachers Assessment*	END SEM University Exam	Teachers Assessment*				
BTCS102N	DC	Principles of 'C'Language	60	20	20	30	20	2	0	2	3

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P – Practical; C - Credit;

***Teacher Assessment** shall be based following components: Quiz/Assignment/ Project/Participation in Class, given that no component shall exceed more than 10 marks.

Course Educational Objectives (CEOs):

The student will have ability to:

1. Identify situations where computational methods and computers would be useful.
2. Give a computational problem, identify and abstract the programming task involved.
3. Approach the programming tasks using techniques learned and write pseudo-code.
4. Choose the right data representation formats based on the requirements of the problem.
5. Use the comparisons and limitations of the various programming constructs and choose the right one for the task in hand.
6. Write the program on a computer, edit, compile, debug, correct, recompile and run it.
7. Identify tasks in which the numerical techniques learned are applicable and apply them to write programs, and hence use computers effectively to solve the task.

Course Outcomes (COs):

After completion of this course the students are expected to be able to demonstrate following knowledge, skills and attitudes. The students will be able to

1. Understand the basic terminologies used in computer programming.
2. Proficient in using the basic constructs of C, to develop a computer program.
3. Understand the use of functions, pointers, arrays and files in programming.
4. Understand the fundamentals of procedure-oriented programming and be able to apply it in computer program development.

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Controller of Examination
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme
Bachelor of Technology (CSE with Specialization in Information and Cyber Security)
SEMESTER- I (2021-2025)

Syllabus

- UNIT I Introduction to Programming Languages:** Evolution of Programming Languages, Structured Programming, The Compilation Process, Object Code, Source Code, Executable Code, Operating Systems, Interpreters, Linkers, Loaders, Fundamentals Of Algorithms, Flowcharts. **7HRS**
- UNIT II Introduction to 'C' Language:** Character Set. Variables and Identifiers, Built-In Data Types. Variable Definition, Arithmetic Operators and Expressions, Constants And Literals, Simple Assignment Statement, Basic Input/ Output Statement, Decision Making Within A Program, Conditions, Relational Operators, Logical Connectives, If Statement, If-Else Statement, Loops: While Loop, Do While, For Loop. Nested Loops, Switch Statement. **10HRS**
- UNIT III Arrays and Pointers:** Array Manipulation; Searching, Insertion, Deletion of an Element from an one dimensional Array; Finding the Largest/Smallest Element in an Array; Two Dimensional Arrays, Addition/Multiplication of Two Matrices, Transpose of a Square Matrix, Address Operators, Pointer Type Declaration, Pointer Assignment, Pointer Initialization, Pointer Arithmetic, Pointer Arrays. **8HRS**
- UNIT IV Functions:** Modular Programming and Functions, Prototype of a Function: Parameter List, Return Type, Function Call, Block Structure, Call by Reference, Call by Value, Recursive Functions and Arrays as Function Arguments **7HRS**
- UNIT V Structure:** Structure Variables, Initialization, Structure Assignment, Structures and Arrays: Arrays of Structures. **8HRS**

Chairperson	Chairperson	Controller of Examination	Joint Registrar
Board of Studies	Faculty of Studies	Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth
Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth	Vishwavidyalaya,Indore	Vishwavidyalaya,Indore
Vishwavidyalaya,Indore	Vishwavidyalaya,Indore		



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme
Bachelor of Technology (CSE with Specialization in Information and Cyber Security)
SEMESTER- I (2021-2025)

Text Books:

1. Gottfried BS – Programming with C, TMH publications.
2. David Griffiths, “Head First C: A Brain-Friendly Guide” O Reilly Media Inc. 2011.
3. Allen B. Tucker, “Programming Languages”, Tata McGraw Hill.
4. Tennence W.Pratt, “Programming languages design and implementation”, Prentice Hall of India.

References:

1. Herbert Schildt “C: Complete Reference”, Tata McGraw Hill 2000.
2. Yashwant Kanetkar, “Let us C”, BPB Publication, 16th Edition 2018.
3. Fundamentals of Programming Languages, R. Bangia, Cyber Tech .
4. Greg Perry and Dean Miller, “C Programming Absolute Beginner’s Guide 3rd Edition”, Que Publishing 2013.

List of Experiments:

1. Write a C program to display “This is my first C Program”.
2. Write a C program to calculate area and circumference of a circle.
3. Write a C program to perform addition, subtraction, division and multiplication of two numbers.
4. Write a program to calculate simple and compound interest.
5. Write a program to swap values of two variables with and without using third variable.
6. Write a program to display the size of every data type using “sizeof” operator.
7. Write a program to illustrate the use of unary prefix and postfix increment and decrement operators.
8. Write a program to input two numbers and display the maximum number.
9. Write a program to find the largest of three numbers using ternary operators.
10. Write a program to find the roots of quadratic equation.
11. Write a program to input name, marks of 5 subjects of a student and display the name of the student, the total marks scored, percentage scored and the class of result.
12. Write a Program to Check Whether a Number is Prime or not.

Chairperson	Chairperson	Controller of Examination	Joint Registrar
Board of Studies	Faculty of Studies	Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth
Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth	Vishwavidyalaya,Indore	Vishwavidyalaya,Indore
Vishwavidyalaya,Indore	Vishwavidyalaya,Indore		



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme

Bachelor of Technology (CSE with Specialization in Information and Cyber Security)

SEMESTER- I (2021-2025)

13. Write a program to find the largest and smallest among three entered numbers and also display whether the identified largest/smallest number is even or odd.
14. Write a program to find the factorial of a number.
15. Write a program to check number is Armstrong or not.

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Controller of Examination
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme
Bachelor of Technology (CSE with Specialization in Information and Cyber Security)

SEMESTER- I (2021-2025)

SUBJECT CODE	Category	SUBJECT NAME	TEACHING & EVALUATION SCHEME								
			THEORY			PRACTICAL		Th	T	P	CRED ITS
			END SEM University Exam	Two Term Exam	Teachers Assessment*	END SEM University Exam	Teachers Assessment*				
BTCS103N	DC	Computer System Organization	60	20	20	30	20	2	0	2	3

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P – Practical; C - Credit;

***Teacher Assessment** shall be based following components: Quiz/Assignment/ Project/Participation in Class, given that no component shall exceed more than 10 marks.

Course Educational Objectives (CEOs):

1. To understand the basic model of a modern computer with its various processing units.
2. To impart knowledge on CPU and it's processing of programs.
3. To provide the information for hardware utilization methodology.
4. To impart knowledge of Multiprocessor and inter-process communication.

Course Outcomes (COs):

After completion of this course the students are expected to be able to demonstrate following knowledge, skills and attitudes.

The students will be able to:

1. Understand the architecture of a modern computer.
2. Explain the functional behavior of CPU and its other processing units.
3. Knowledge of the Peripherals of a Computer System.
4. Give the information to speed-up the working of Computer System.

Syllabus

Unit I

10HRS

Computer Basics: Von Newman model, CPU, Memory, I/O, Bus, Memory registers, Program Counter, Accumulator, Instruction register, Micro-operations, Register Transfer Language, Instruction cycle, Instruction formats and addressing modes.

Chairperson	Chairperson	Controller of Examination	Joint Registrar
Board of Studies	Faculty of Studies	Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth
Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth	Vishwavidyalaya,Indore	Vishwavidyalaya,Indore
Vishwavidyalaya,Indore	Vishwavidyalaya,Indore		



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme

Bachelor of Technology (CSE with Specialization in Information and Cyber Security)

SEMESTER- I (2021-2025)

Unit II 9HRS

Control Unit Organization: Hardwired control unit, Micro-programmed control unit, Control Memory, Address Sequencing, Micro Instruction formats, Micro program sequencer, Microprogramming. **Arithmetic and Logic Unit:** Arithmetic Processor, Addition, subtraction, multiplication, and division, Floating point, and decimal arithmetic.

Unit-III 8HRS

Input Output Organization: Modes of data transfer – program controlled, interrupt driven and direct memory access, Interrupt structures, I/O Interface, Asynchronous data transfer, I/O processor, Data transferring approaches and modes.

Unit-IV 7HRS

Memory organization: Memory Hierarchy, Cache Memory - Organization and types of cache mappings, Virtual memory, Memory Management Hardware.

Unit-V 8HRS

Multiprocessors: Pipeline and Vector processing, Instruction and arithmetic pipelines, Vector and array processors, Interconnection structure and inter-processor communication.

Text Books:

1. M. Morris Mano, Computer System Architecture, Fourth edition, Pearson Education, 2015.
2. William Stallings, Computer Organization and Architecture, Seventh Edition, PHI, 2009.
3. Andrew S. Tanenbaum, Structured Computer Organization, Sixth Edition, Pearson Education, 2016.
4. John P. Hayes, Computer Architecture and Organizations, Third edition, Mc-Graw Hills, New Delhi, 2017

Chairperson	Chairperson	Controller of Examination	Joint Registrar
Board of Studies	Faculty of Studies	Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth
Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth	Vishwavidyalaya, Indore	Vishwavidyalaya, Indore
Vishwavidyalaya, Indore	Vishwavidyalaya, Indore		



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme

Bachelor of Technology (CSE with Specialization in Information and Cyber Security)

SEMESTER- I (2021-2025)

References:

1. John L. Hennessy and David A. Patterson, Computer Architecture a quantitative approach, Fourth Edition, Elsevier, 2007.
2. Ramesh Gaonkar, Microprocessor Architecture, Programming and Applications with 8085, fifth Edition, Prentice Hall, 2015.
3. Nicholas Carter, Computer Architecture (Schaum's), Third Edition, TMH, 2012.
4. Carl Hamacher, Computer Organization, Fifth Edition, TMH, 2002.

List of Experiments:

1. Study of peripherals, components of a Computer System.
2. Write a C program for sum of two binary numbers.
3. Write a C program for multiplication of two binary numbers.
4. Write a C program to implement Booth's algorithm for multiplication.
5. Write a C program to implement Restoring Division Algorithm.
6. Write the working of 8085 simulator GNUsim8085 and basic architecture of 8085 along with small introduction.
7. Study the complete instruction set of 8085 and write the instructions in the instruction set of 8085 along with examples.
8. Write an assembly language code in GNUsim8085 to implement data transfer instruction.
9. Write an assembly language code in GNUsim8085 to store numbers in reverse order in memory location.
10. Write an assembly language code in GNUsim8085 to add two 8 bit numbers stored in memory and also storing the carry.

Chairperson	Chairperson	Controller of Examination	Joint Registrar
Board of Studies	Faculty of Studies	Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth
Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth	Vishwavidyalaya,Indore	Vishwavidyalaya,Indore
Vishwavidyalaya,Indore	Vishwavidyalaya,Indore		



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme
Bachelor of Technology (CSE with Specialization in Information and Cyber Security)
SEMESTER- I (2021-2025)

SUBJECT CODE	Category	SUBJECT NAME	TEACHING & EVALUATION SCHEME								
			THEORY			PRACTICAL		Th	T	P	CRED ITS
			END SEM University Exam	Two Term Exam	Teachers Assessment*	END SEM University Exam	Teachers Assessment*				
BTIT102N	DC	Web Development Lab-I(XML & HTML)	0	0	0	30	20	0	0	2	1

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P – Practical; C - Credit;
***Teacher Assessment** shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Course Educational Objectives (CEOs):

1. Students will use a variety of design software to organize, create, publish, and manage a web site.
2. This course also puts emphasis on basic concepts of web design
3. Provide you the conceptual and technological developments in the field of Internet and web designing.
4. Course content includes creating a variety of graphic elements including video, animations, rollover effects, backgrounds, and page images

Course Outcomes:

1. Create an HTML Documents, and establish adequate formatting for presentation purposes.
2. Import, insert and modify images.
3. Insert and manipulate tables.
4. Establish and maintain internal and external link to available resources.
5. Use special effect to make the expressive, evocative documents.
6. Insert and manipulate multi-media objects.

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Controller of Examination
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme
Bachelor of Technology (CSE with Specialization in Information and Cyber Security)
SEMESTER- I (2021-2025)

Syllabus:

UNIT I

10 HRS

Introduction to HTML: What is HTML, HTML Documents, SGML, Basic structure of an HTML document, creating an HTML document, Headers tags, Body tags, Paragraphs formatting, Text Elements, Tag Elements, Special Character elements, Image tags, HTML Table tags and lists: Numbered list, Non-Numbered lists, Definition lists, Anchor tag, Name tag, Hyperlinks – FTP/HTTP/HTTPS, Links with images and buttons, Links to send email messages, Text fonts and styles, background colors/images, Marquee Behavior, Forms related tags. (Action, method, name, input etc.)

UNIT II

9 HRS

HTML5: Introduction of HTML5, Browser supports, Migration from HTML4 to HTML5, New Elements in HTML5, HTML5 different parts layout of a web page, HTML5 Graphics: Canvas, SVG, HTML Media Tags: Inserting audio files, Inserting video files, Screen control attributes, Media control attributes, HTML Object.

UNIT III

8 HRS

CSS: Introduction of CSS, CSS Syntax CSS Id & Class. CSS Styling: styling Backgrounds, styling Text, styling Fonts, styling Links, styling Lists, styling Tables. CSS Box Model: Border, Outline, Margin, Padding. CSS Advanced: Grouping/Nesting, Dimension, Display, Positioning, Floating, Align, Pseudo-class, Pseudo-element, Navigation Bar, Image Gallery, Image capacity, Image Sprites, Media Types, and Attribute Selectors.

UNIT IV

7 HRS

XML: Introduction of XML, Cross scripting of XML, XML as intermediate language, Difference between XML and HTML, XML DOM, Tree, Syntax, Elements, Attributes, Namespaces, XPath, XML DTD, Applications, XQuery, XML Schema, XML Parser, XHTML: Introduction of XHTML, XHTML rules over the HTML, conversation HTML to XHTML.

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Controller of Examination
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme

Bachelor of Technology (CSE with Specialization in Information and Cyber Security)

SEMESTER- I (2021-2025)

UNIT V

8 HRS

Java Script: Introduction to client side scripting, Server side scripting, Java Script Syntax, Variables and Functions, Operators: JavaScript Arithmetic Operators, JavaScript Assignment Operators, JavaScript Popup Boxes, JavaScript Window, Events and Objects, JavaScript Function Call, Validation in webpages, Introduction of AJAX.

TEXT BOOKS:

1. Jennifer Niederst Robbins. Learning Web Design, Fifth Edition, O'Reilly Media, Inc, May 2018.
2. Frain and Ben. Responsive Web Design with HTML5 and CSS3, Second Edition, 2015.
3. Nicholas c.Zakas. Java Script for Web Developers, Third edition, 2012.
4. George Q. Huang, K. L Mak. Internet Applications in Product Design and Manufacturing, ISBN: 3540434658, 2003 edition, springer, 2012.

REFERENCES:-

1. Steven M. Schafer, "HTML, XHTML, and CSS Bible", Fifth Edition, WileyIndia, 2010.
2. John Duckett,"Beginning HTML, XHTML, CSS, and JavaScript ",WileyIndia, 2010.
3. Ian Pouncey, Richard York, "Beginning CSS: Cascading Style Sheets for Web Design",3rd edition, Wiley India, 2011.
4. Achyut S. Godbole, Atul Kahate, Web Technologies, ISBN: 9781259062681,3rd edition, TMH, 2013

LIST OF PRACTICALS:

Chairperson	Chairperson	Controller of Examination	Joint Registrar
Board of Studies	Faculty of Studies	Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth
Shri Vaishnav Vidyapeeth	Shri Vaishnav Vidyapeeth	Vishwavidyalaya,Indore	Vishwavidyalaya,Indore
Vishwavidyalaya,Indore	Vishwavidyalaya,Indore		



Shri Vaishnav Vidyapeeth Vishwavidyalaya

Shri Vaishnav Institute of Information Technology

Choice Based Credit System (CBCS) Scheme

Bachelor of Technology (CSE with Specialization in Information and Cyber Security)

SEMESTER- I (2021-2025)

1. Design a Web Page, Insert an image on to the web page such that image is of height 300 and width 300 pixels. The image should have an ALT text in it.
2. Create a Web page that holds a bulleted list of the names of your friends. Make sure that the bullets are in plain circle.
3. Create a Frame which would hold both the web page that was created earlier. The frame should be split row-wise into equal halves.
4. Create a Web Page to display the marks you got in all subjects of last semester using table.
5. Create a Form having two boxes with labels as First Name and Last Name. The User should not be allowed to enter the names directly in the text boxes. The input has to be given in the prompt box and then entered values should be given in the textboxes.
6. Create a Web Page that has a button in the center of the page. Using mouse events change the Message in the statusbar.
7. Design a Web page that accepts Username and Password. Opens a new window when the password corresponds to a particular value is set by the developer
8. Design a Web page that consists of 2 text boxes. When the page is first loaded set the focus to the first textbox. The user should not be allowed to leave the box unless enters a value in it.
9. To convert the HTML code to XHTML code.
10. To study the XML tree.
11. To study of Dreamweaver Tool.
12. To study of a Flash Animation Tool

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Controller of Examination
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore

Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya,Indore