


Shri Vaishnav Vidyapeeth Vishwavidyalaya													
Choice Based Credit System (CBCS)													
Master of Technology (Power Electronics)(Session 2018-19)													
III-SEMESTER													
Sn	Subject Code	Subject Name	Course Code	Teaching Scheme/week			Examination Scheme					Total Marks	Total Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Assessment*	End Sem University Exam	Teachers Assessment*		
1	MBAI202	Reasearch Methodology		3	1	0	60	20	20	0	0	100	4
2	MBAI301C	Human Values and Professional Ethics		3	1	0	60	20	20	0	0	100	4
3	MTPS301	Dissertation Phase-I		0	0	24	0	0	0	180	120	300	12
Total				6	2	24	120	40	40	180	120	500	20

  
**Chairperson**  
**Board of Studies**  
 Shri Vaishnav Vidyapeeth Vishwavidyalaya  
 Indore

  
**Registrar**  
 Shri Vaishnav Vidyapeeth Vishwavidyalaya  
 INDORE (M.P.)