

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore
Shri Vaishnav Institute of Information Technology

Time Table
V Semester End Semester Examination Dec. - Jan. 2023-24
B.Tech. (All Programs Including Dual Degree) Without NEP Scheme

Time: 02:00 PM to 5:00 PM

Date/Day	B.Tech. (Computer & Communication Engineering) / B.Tech. (Computer Science Engineering)	B.Tech. (CSE with specialization in Enterprise system in association with Red Hat)	B.Tech. + M.B.A. (CSE) / B.Tech. + M.Tech. (CSE / CSE-CC / CSE-CF / CSE-BDA)	B.Tech. (CSE with Mobile Applications -Apple Authorized Training Center)	B.Tech. (CSE with specialization Big Data Analytics in association with IBM / B.Tech. (CSE with Cloud and Mobile Computing In Association with IBM)	B.Tech. (IBM - DS)	B.Tech. (Full Stack Developer)	B.Tech. (CSE with specialization in Artificial Intelligence in association with IBM)	B.Tech. (Computer Science and Business System -TCS)	B.Tech (BDCE- Impetus Technologies)	B.Tech./B.Tech. + M.B.A. / B.Tech. + M.Tech. (Information Technology)	B.TECH (CSE with specialization in Information & Cyber Security)
19/12/2023 Tuesday									BTCSCS301			
									Design and Analysis of Algorithms			
21/12/2023 Thursday	BTCS511	BTCS511	BTCS511	BTCS511	BTIBM602	BTIBM602	BTIBM402	BTIBM402	BTCSCS302	BTCS505	BTCS511	BTCS511
	Artificial Intelligence	Artificial Intelligence	Artificial Intelligence	Artificial Intelligence	Big Data Technologies (Hadoop)	Big Data Technologies (Hadoop)	Cyber Physical System Using IoT	Cyber Physical System Using IOT	Compiler Design	Big Data Analytics	Artificial Intelligence	Artificial Intelligence
23/12/2023 Saturday	BTIT603		BTIT603		BTIT603	BTIT603	BTIT603	BTIT603		BTIT603	BTIT603	BTICS501
	Cyber and Network Security		Cyber and Network Security		Cyber and Network Security	Cyber and Network Security	Cyber and Network Security	Cyber and Network Security		Cyber and Network Security	Cyber and Network Security	Network Security and Cryptography
27/12/2023 Wednesday	BTCS503		BTCS503		BTCS503	BTCS503	BTCS503	BTCS503		BTCS503	BTCS503	BTCS503
	Computer Graphics and Multimedia		Computer Graphics and Multimedia		Computer Graphics and Multimedia	Computer Graphics and Multimedia	Computer Graphics and Multimedia	Computer Graphics and Multimedia		Computer Graphics and Multimedia	Computer Graphics and Multimedia	Computer Graphics and Multimedia
29/12/2023 Friday		BTIT611		BTIT611							BTIT611	BTIT611
		Information Storage and Management		Information Storage and Management							Information Storage and Management	Information Storage and Management
02/01/2024 Tuesday	BTCS504	BTCS504	BTCS504	BTCS504	BTCS504	BTCS504	BTCS504	BTCS504	BTCSH3063	BTCS504	BTIT713	BTCS504
	Software Engineering and Project Management	Software Engineering and Project Management	Software Engineering and Project Management	Software Engineering and Project Management	Software Engineering and Project Management	Software Engineering and Project Management	Software Engineering and Project Management	Software Engineering and Project Management	Machine Learning	Software Engineering and Project Management	Distributed Systems	Software Engineering and Project Management

Practical Examination to be held between 04 Dec. to 15 Dec. 2023

*Students are advised to see their internal marks before appearing in the end sem examination

The syllabus for ATKT students shall be as per the revised (updated) syllabus.



Mr. Sunil Pipleva
Asst. COE



Dr. Nilesh Patidar
Asst. COE



Dr. Shishir Jain
Controller of Examinations

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Shri Vaishnav Institute of Technology and Science, Indore

Time Table

V Semester End Semester Examination Dec. - Jan. 2023-24

B.Tech. (All Programs Including Dual Degree) Without NEP Scheme

Time: 02:00 PM to 5:00 PM

Date/Day	B.Tech (Mechanical Engineering)(2020 Scheme)	B.Tech (Automobile Engineering) (2020 Scheme)	B.Tech (Electronics & Communications Engineering)(2020 Scheme)	B.Tech (Electronics & Communications Engineering)+(IOT) (2020 Scheme)	B.Tech (EI) (2020 Scheme)	B.Tech (Mechatronics) (2020 Scheme)	B.Tech (Robotics and Automation) (2020 Scheme)	B.Tech ((Electrical Engineering/ Electrical & Electronics) (2020)	B.Tech (Civil Engineering) 2020 Admitted Batch
19/12/2023 Tuesday	BTME501	BTAU501	BTEC501	BTEC501	BTEI501	BTMT502	BTEC501	BTEE501	BTCE501
	Operation Research and Supply Chain	Supply Chain Management & OR	Microprocessor, Microcontroller and Interfacing	Microprocessor, Microcontroller and Interfacing	Digital Signal Processing	Introduction to Robotics	Microprocessor, Microcontroller and Interfacing	Electrical Machines-II	Structural Analysis - I
21/12/2023 Thursday	BTME502	BTME502	BTEC502	BTCS405	BTEI502	BTMT501	BTMT501	BTEE502	BTCE502
	Metrology and Mechanical Measurement	Metrology and Mechanical Measurement	Cellular and Mobile Communication	Data Base Management System	Fibre Optic Instrumentation	Applied Hydraulics and Pneumatics	Applied Hydraulics and Pneumatics	Power Electronics	Transportation Engineering
23/12/2023 Saturday	BTME503	BTAU503	BTEC503	BTECIOT501	BTEE503	BTEE503	BTME503	BTEE503	BTCE503
	Dynamics of Machine	Automotive Transmission System	Digital Communication	Communication Systems	Control System Engineering	Control System Engineering	Dynamics of Machine	Control System Engineering	Geotechnical Engineering - I
27/12/2023 Wednesday	BTME504	BTAU504					BTEE502	BTRA501	BTEE504
	Internal Combustion Engines	Manufacturing Technology					Power Electronics	Robotics Engineering	Switchgear and Protection
29/12/2023 Friday	BTME505	BTME505	BTEC515	BTEC515	BTMT511	BTMT511	BTRA512	BTEE514	BTCE505(2)
	CAD/CAM/CIM	CAD/CAM/CIM	Data Communication and Computer Networks	Data Communication and Computer Networks	Automation	Automation	Industrial Automation	Introduction of IOT in Electrical Engineering	Hydraulics & Hydraulics Machines
02/01/2024 Tuesday				BTECIOT502					
				Wireless And Mobile Communication					
04/01/2024 Thursday			BTEC504	BTEC504	BTEC504				
			CMOS VLSI Design	CMOS VLSI Design	CMOS VLSI Design				

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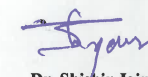
Mr. Sunil Pipleya

Asst. COE



Dr. Nitesh Patidar

Asst. COE



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Controller of Examinations